

TA880GU3+

A88PH-M3T

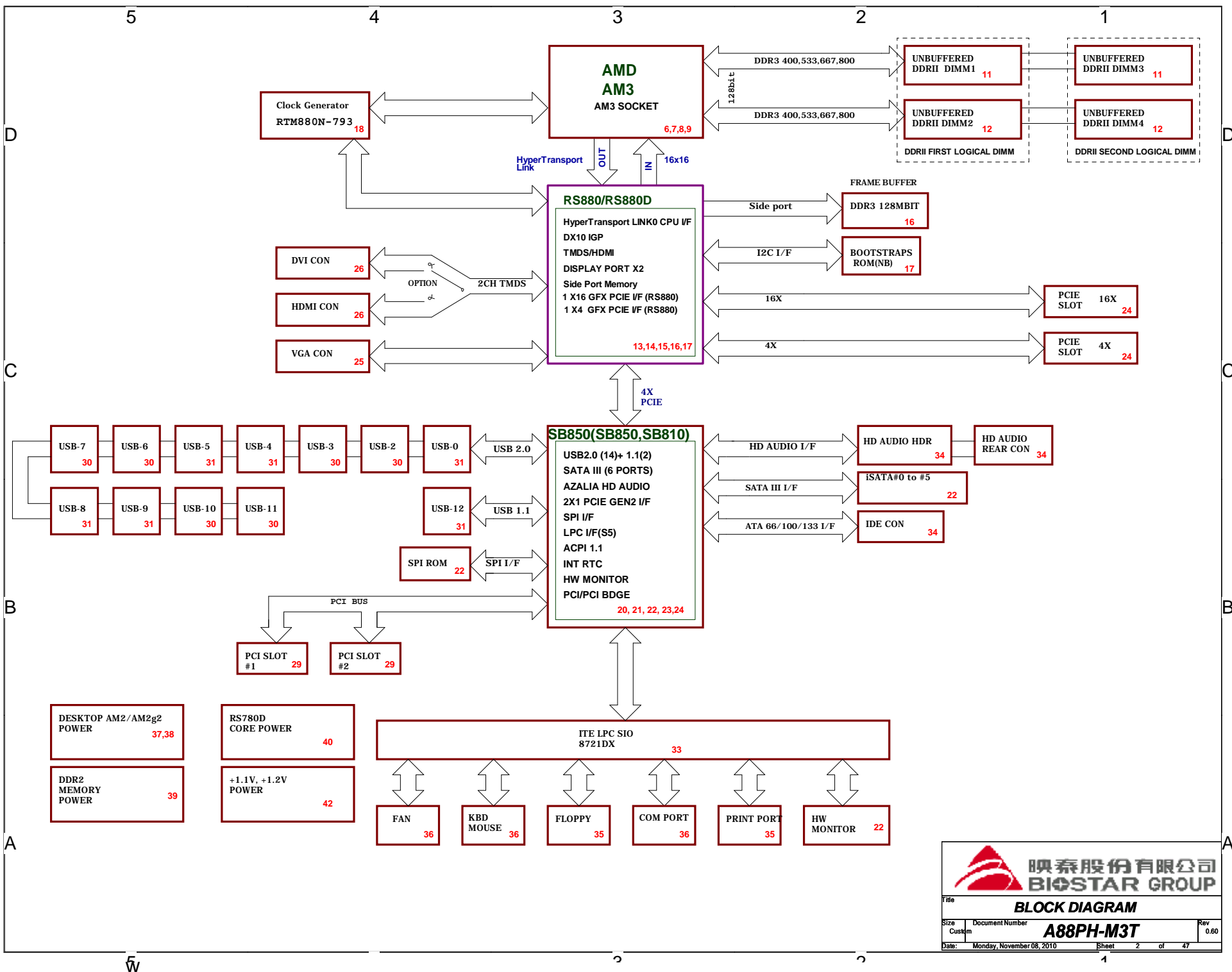
(RS880G/890GX&SB810/850)

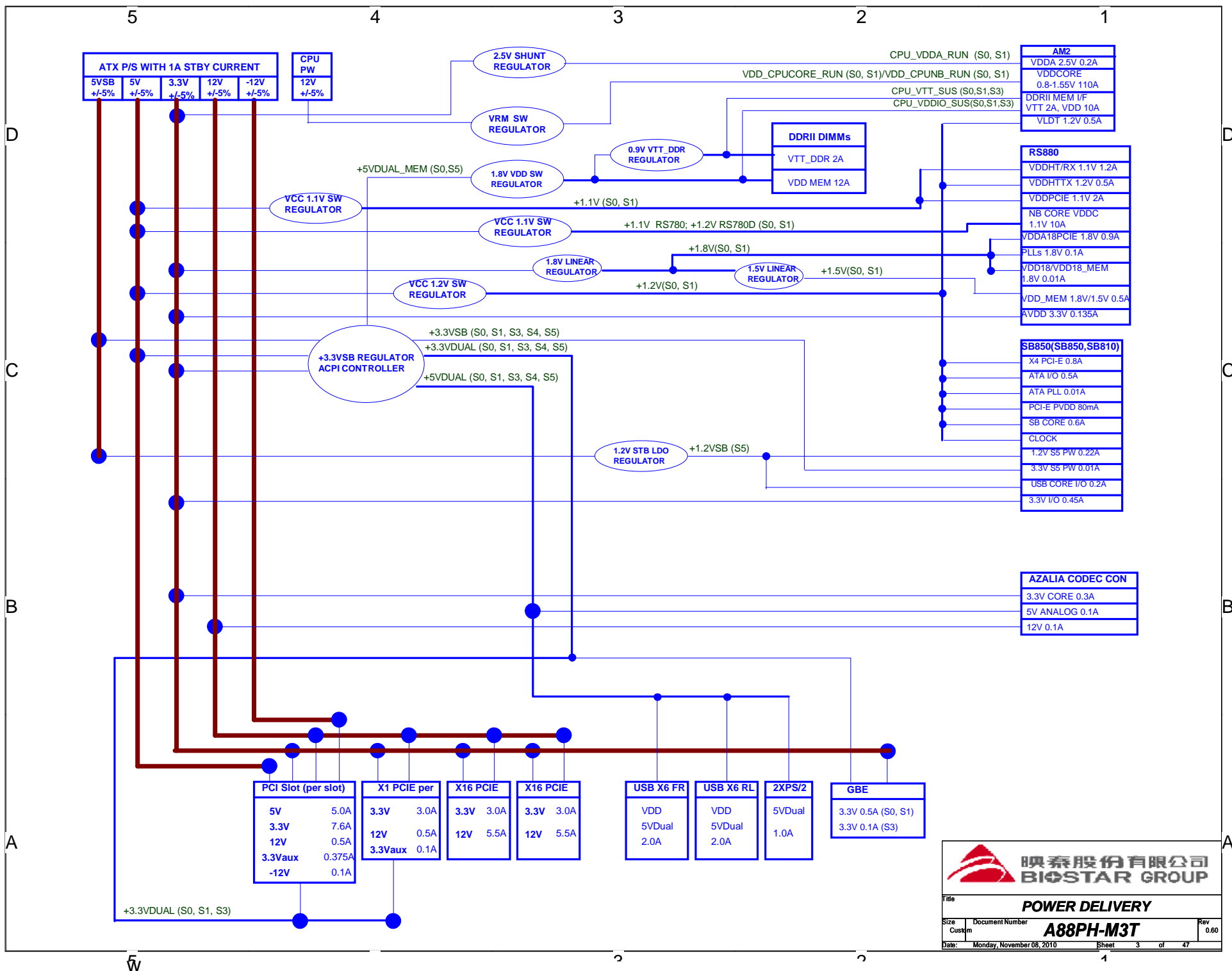
REV 0.60

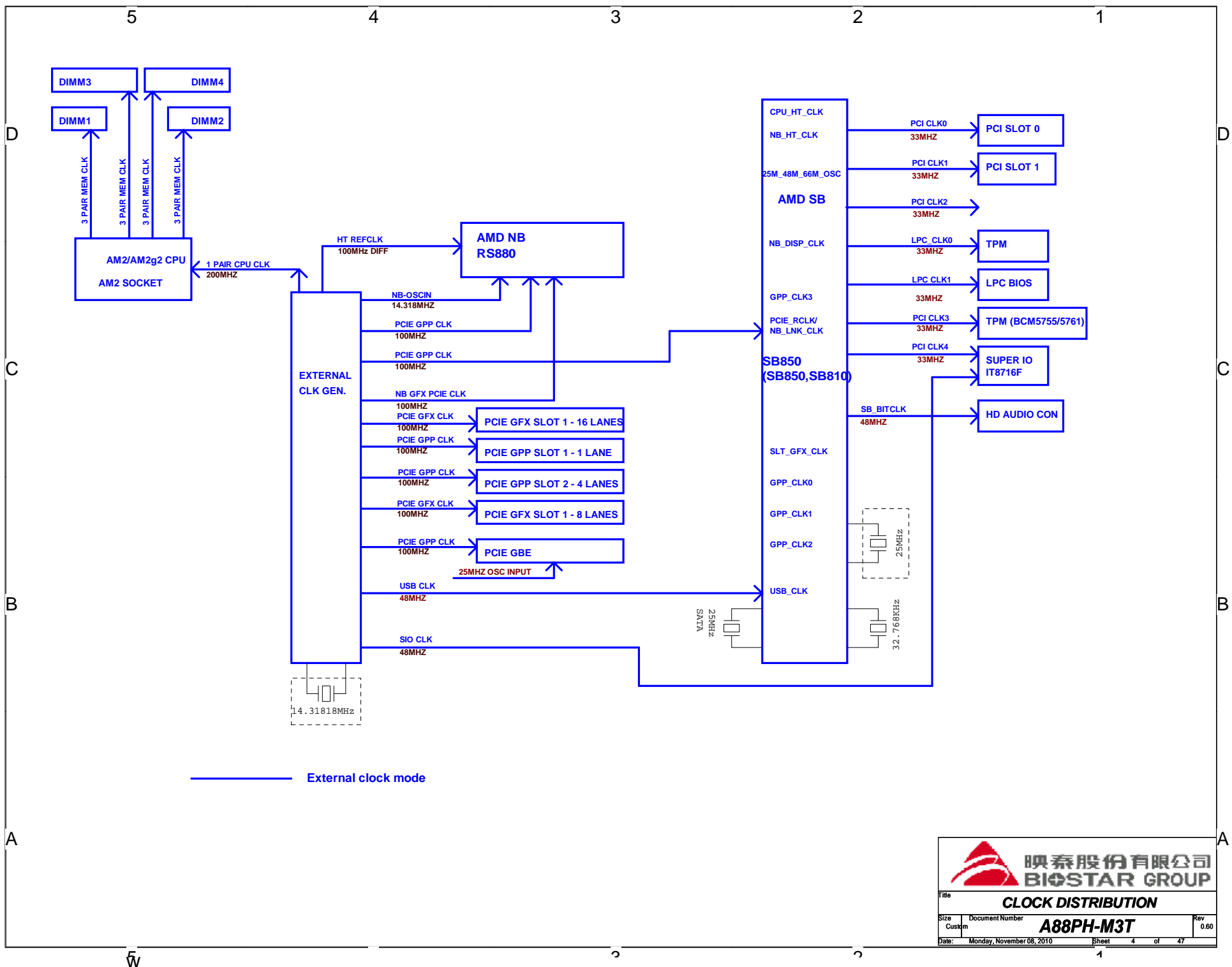
DDR3 X 4 Dual channel , PCI-Ex16 X 1 , PCI X 2
,RELTEK 10/100/1000 PCI-E Lan , AMD K8-940

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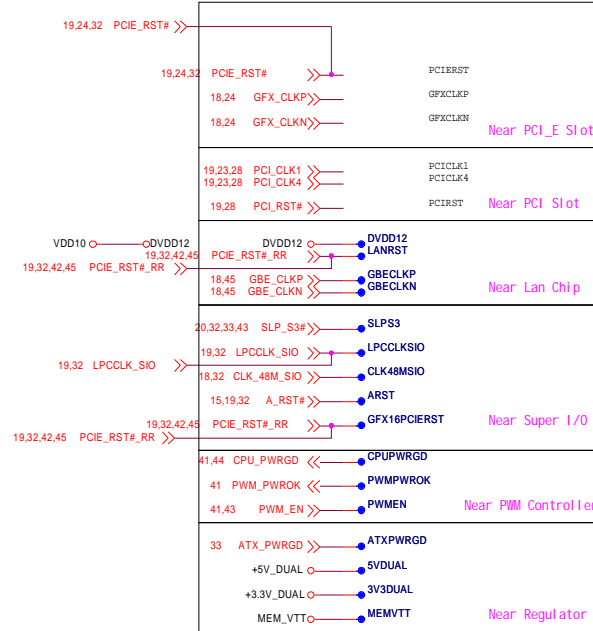
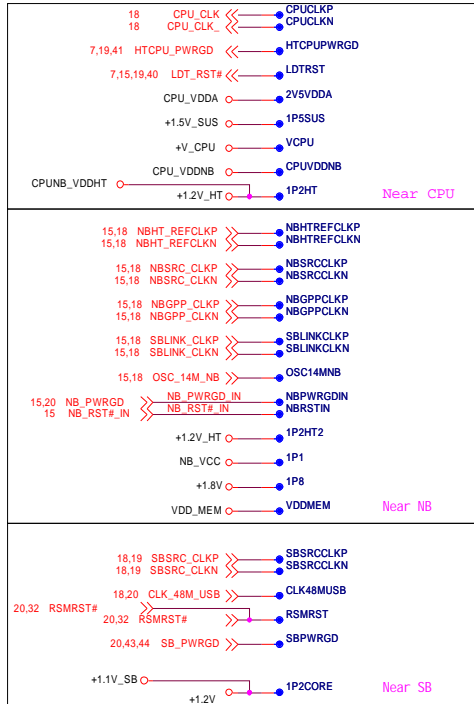
			
映泰股份有限公司 BIOSTAR GROUP			
File COVER			
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Testing Point for Socke AM3 platform

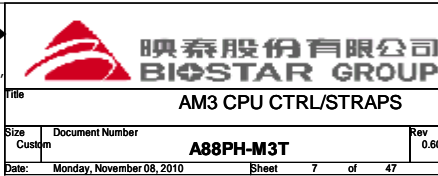


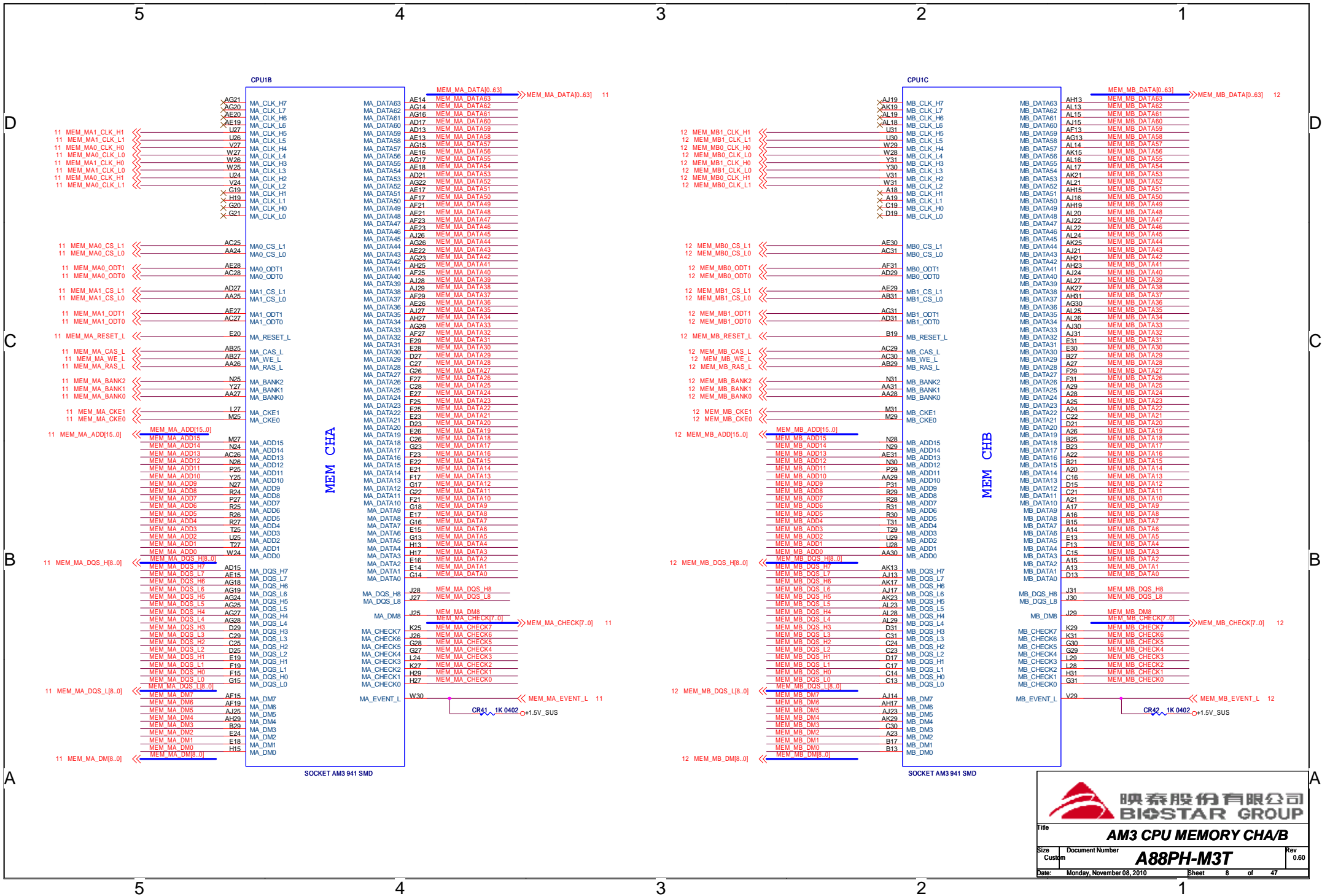
HyperTransport 3.0

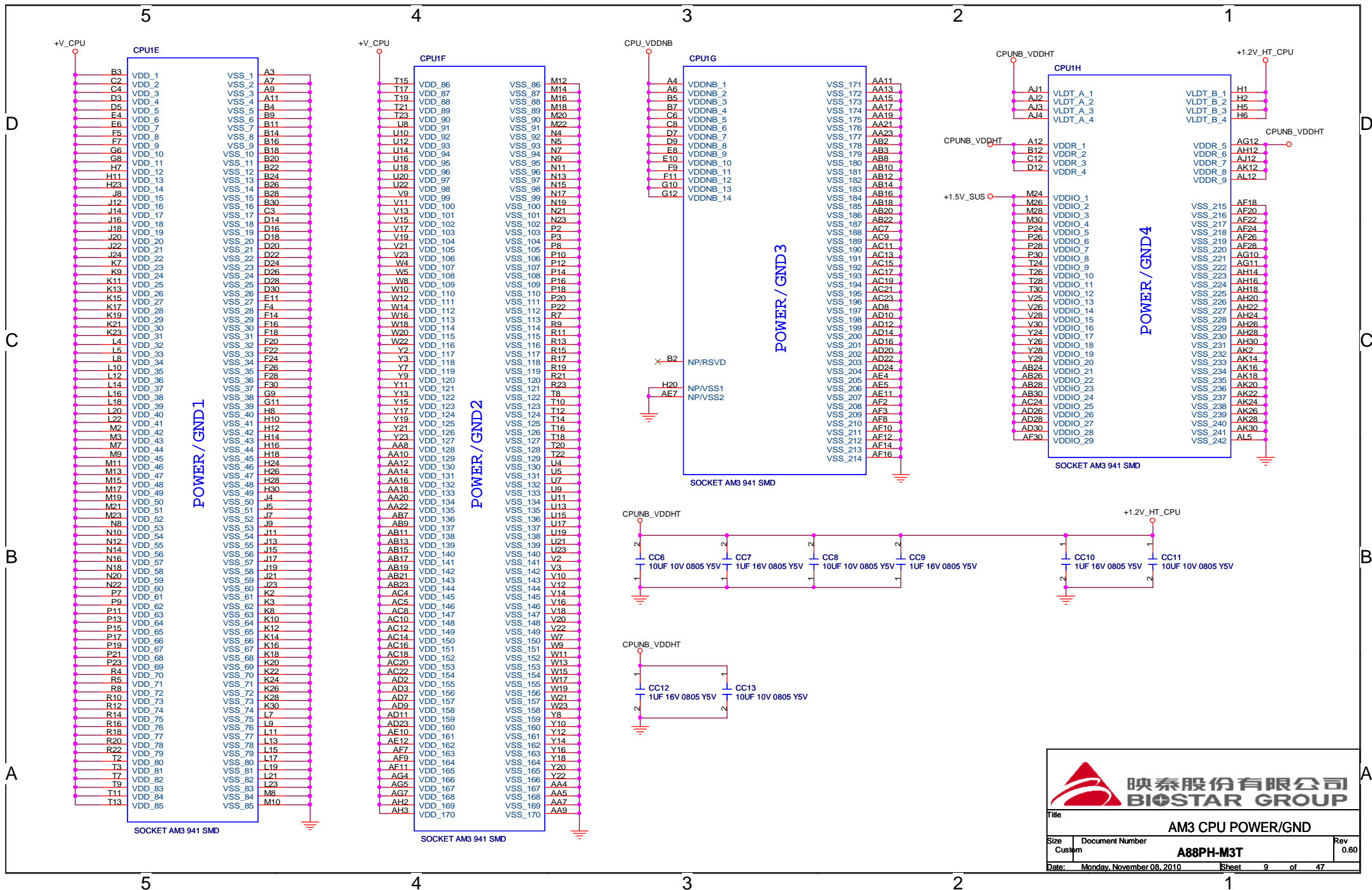


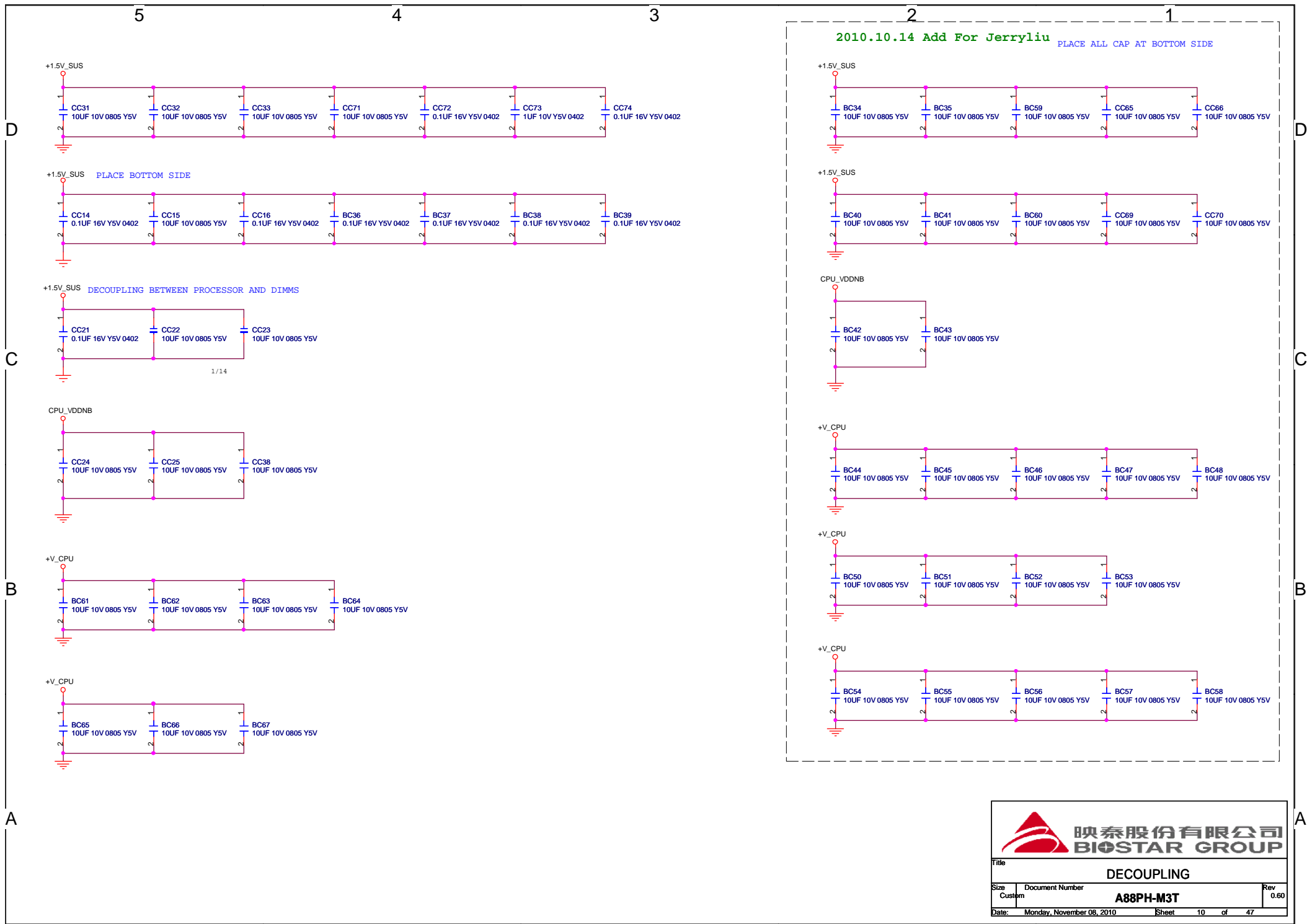
映泰股份有限公司
BIOSSTAR GROUP

Title			AM3 CPU HT		
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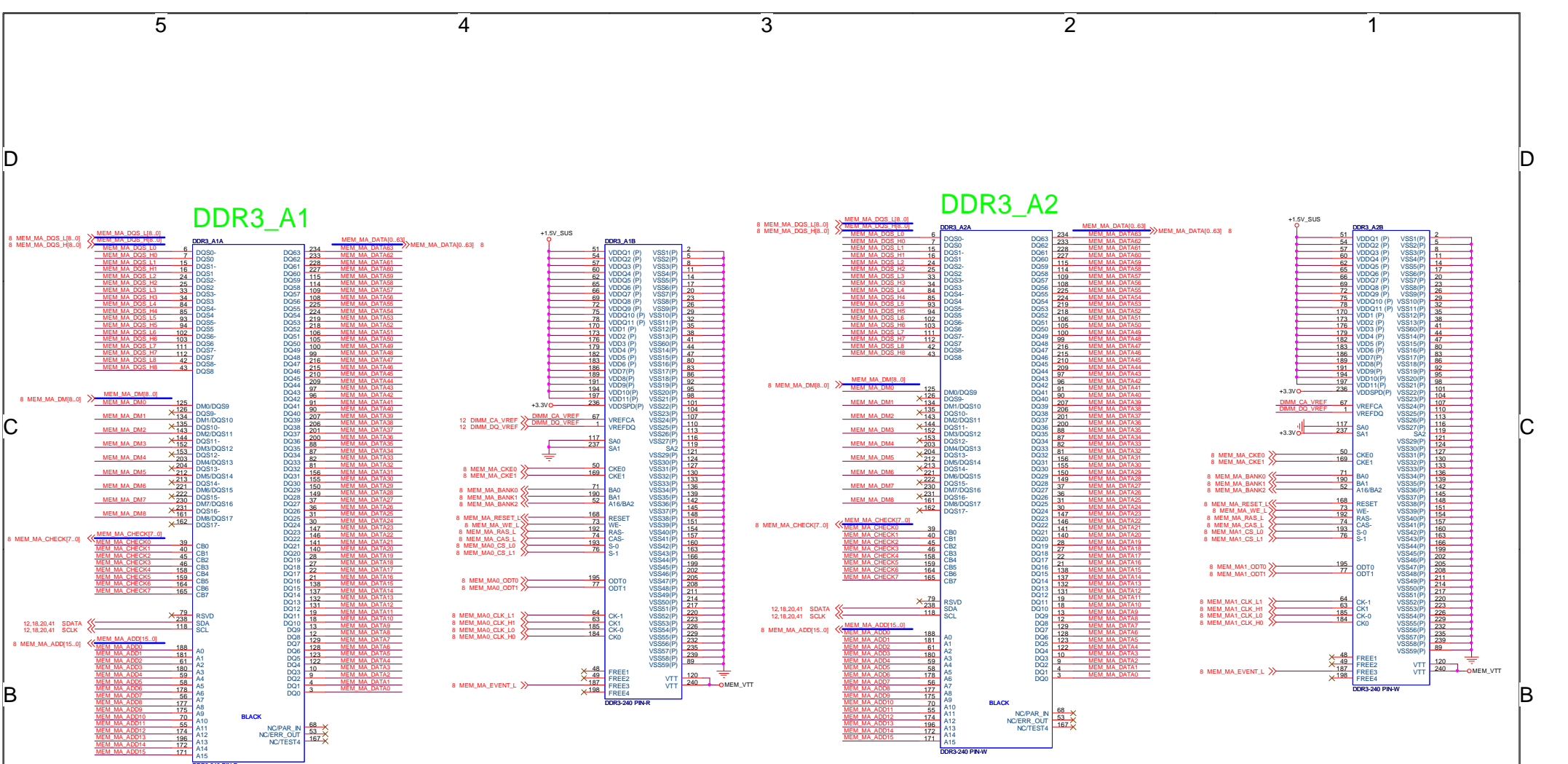


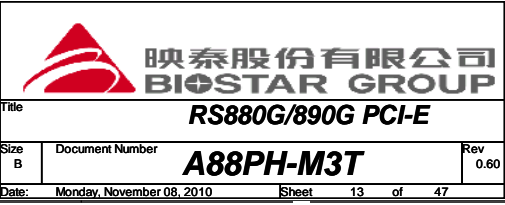


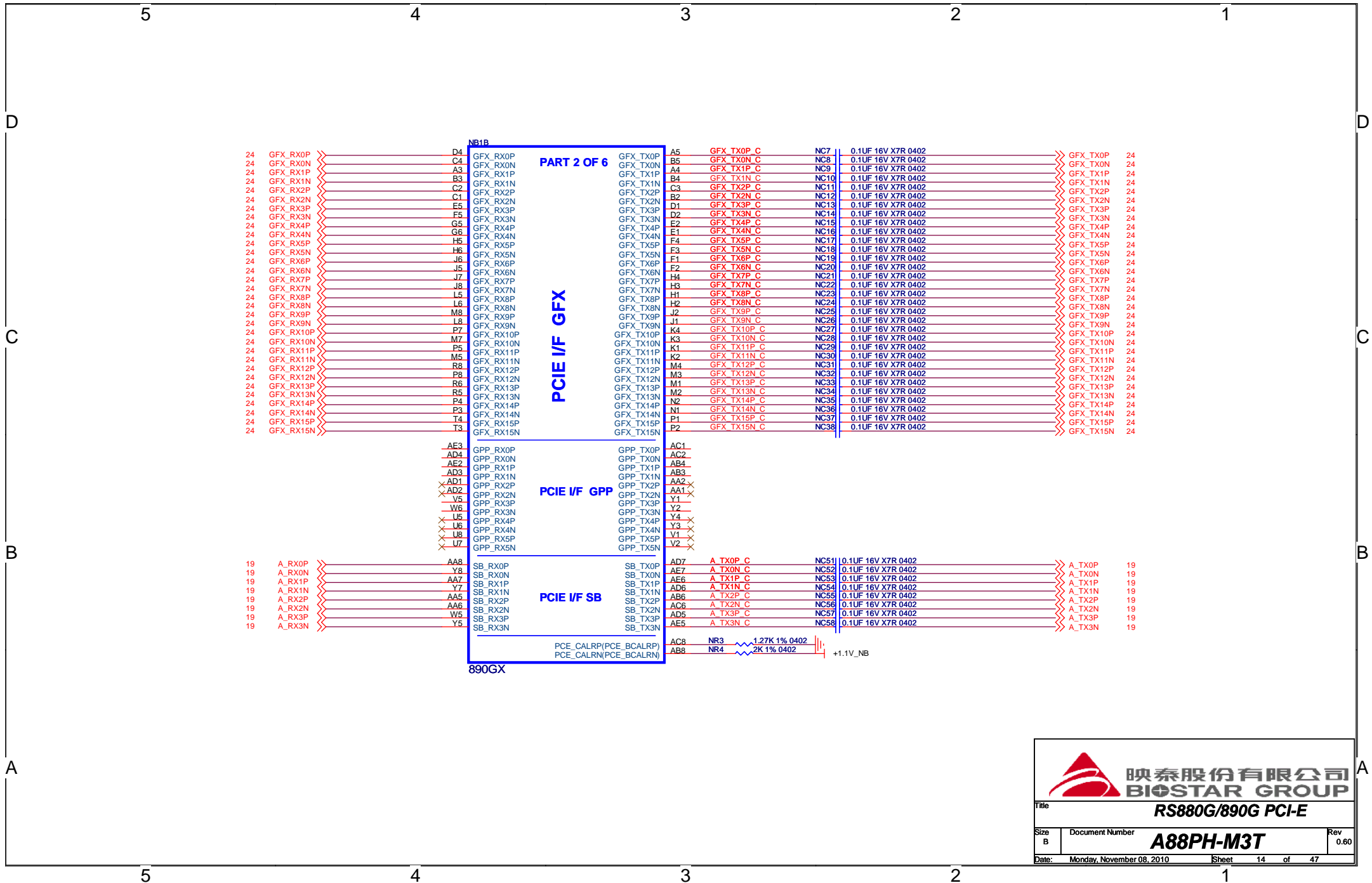


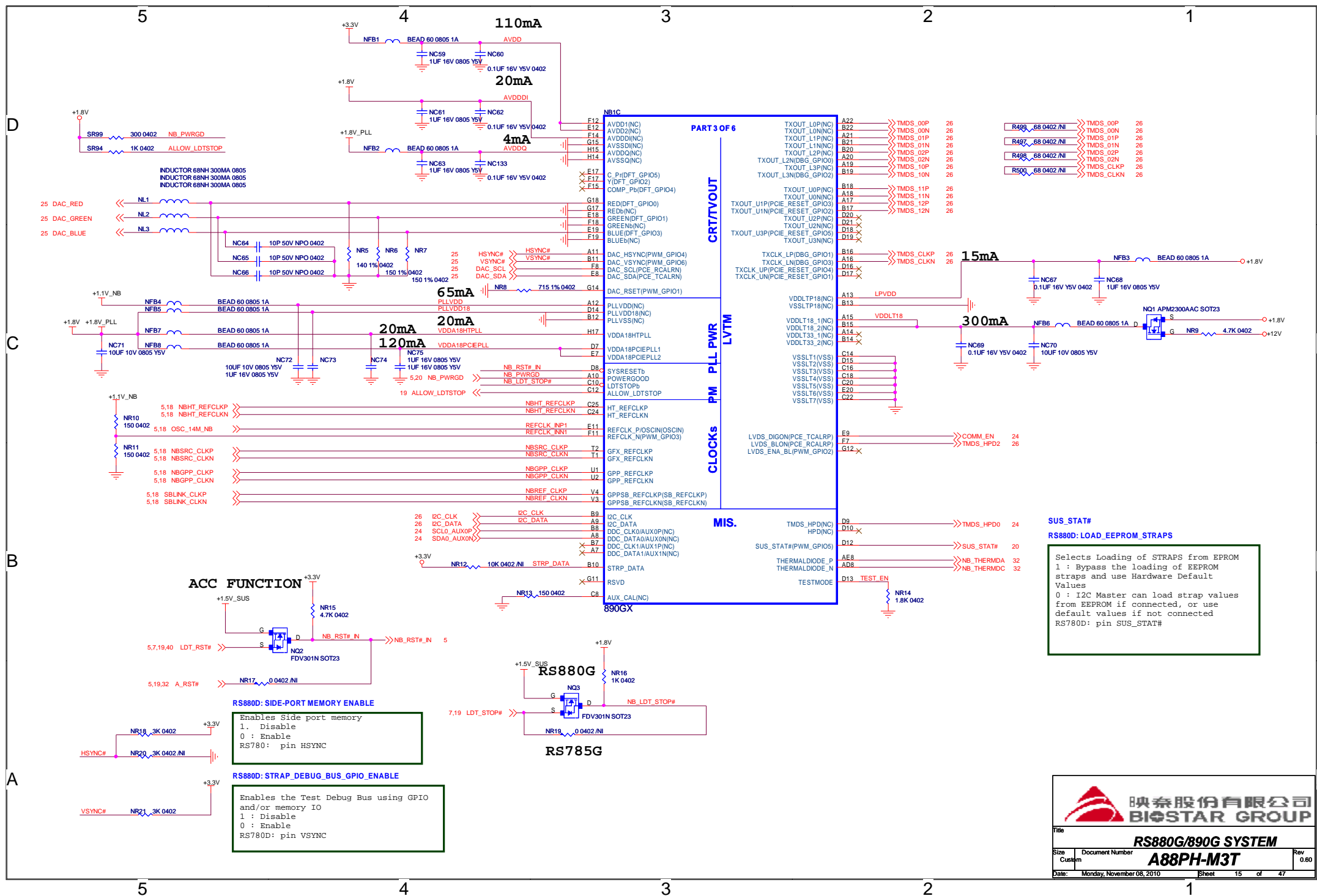


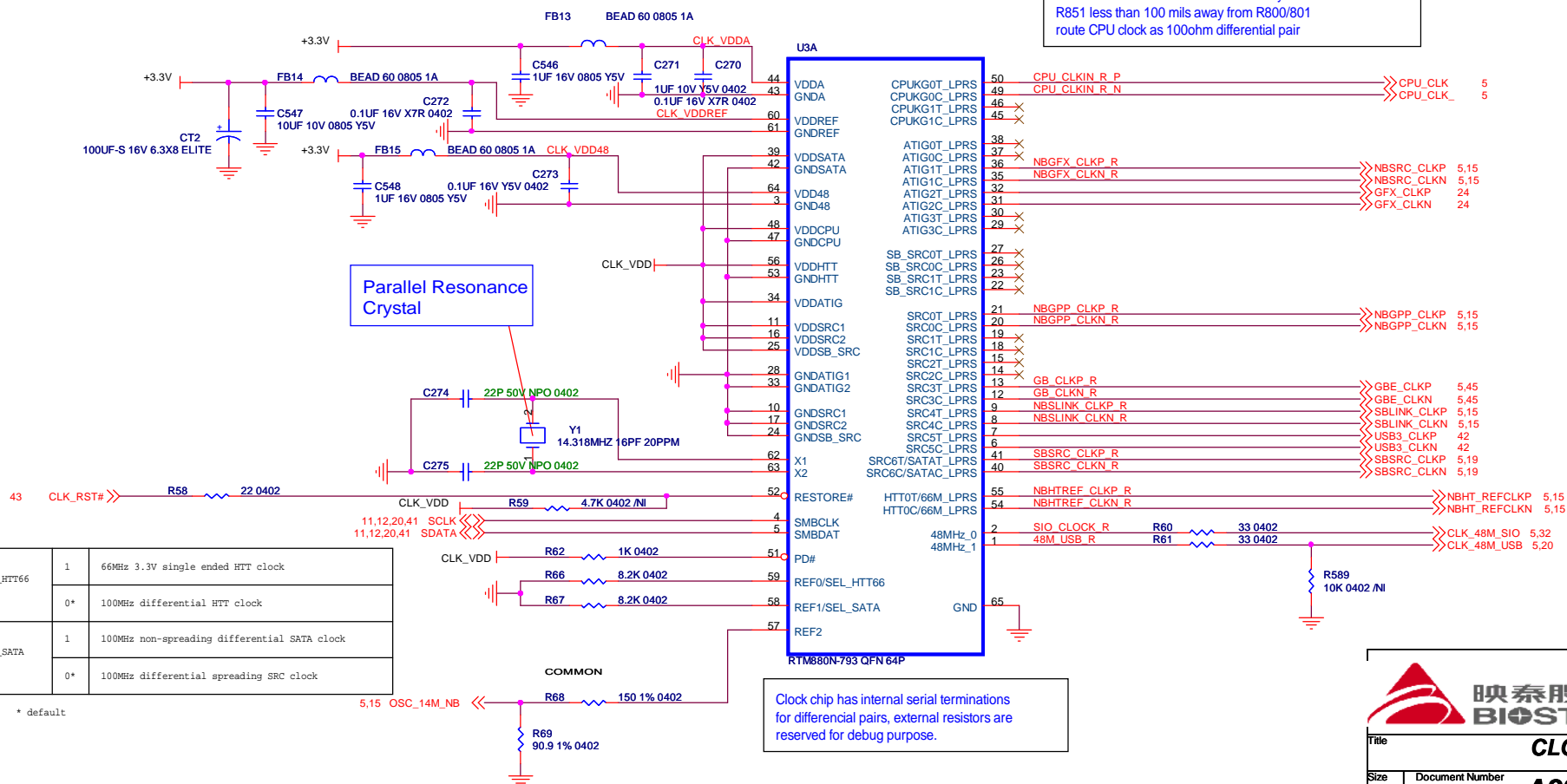
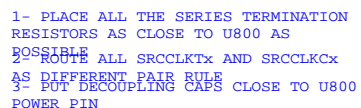
DDR3_A1











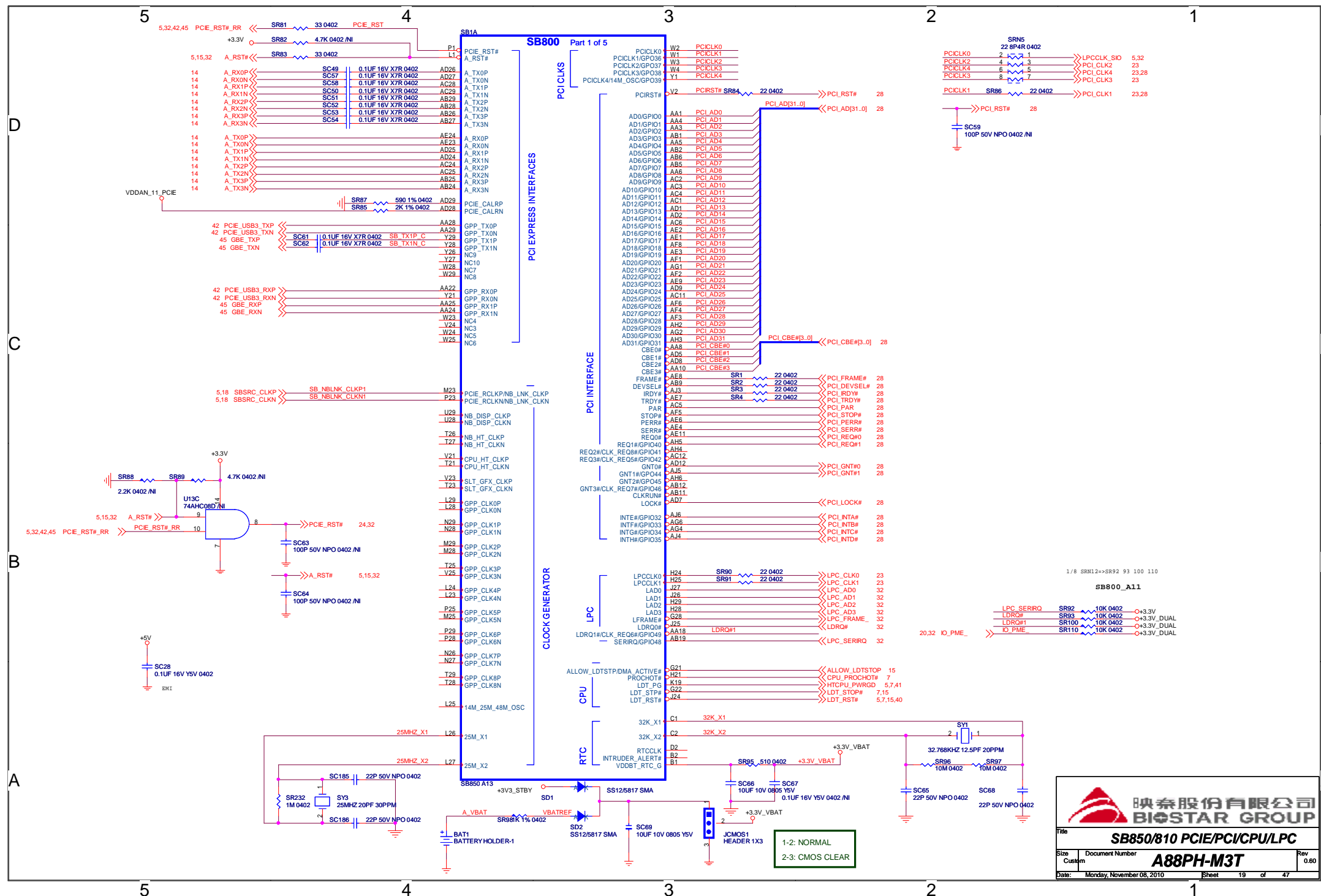
SEL_HTT66	1	66MHz 3.3V single ended HTT clock
	0*	100MHz differential HTT clock
SEL_SATA	1	100MHz non-spreading differential SATA clock
	0*	100MHz differential spreading SRC clock

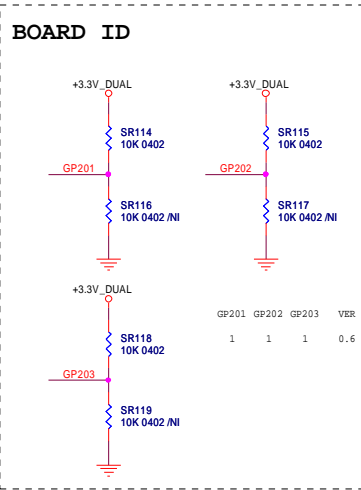
* default

Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.

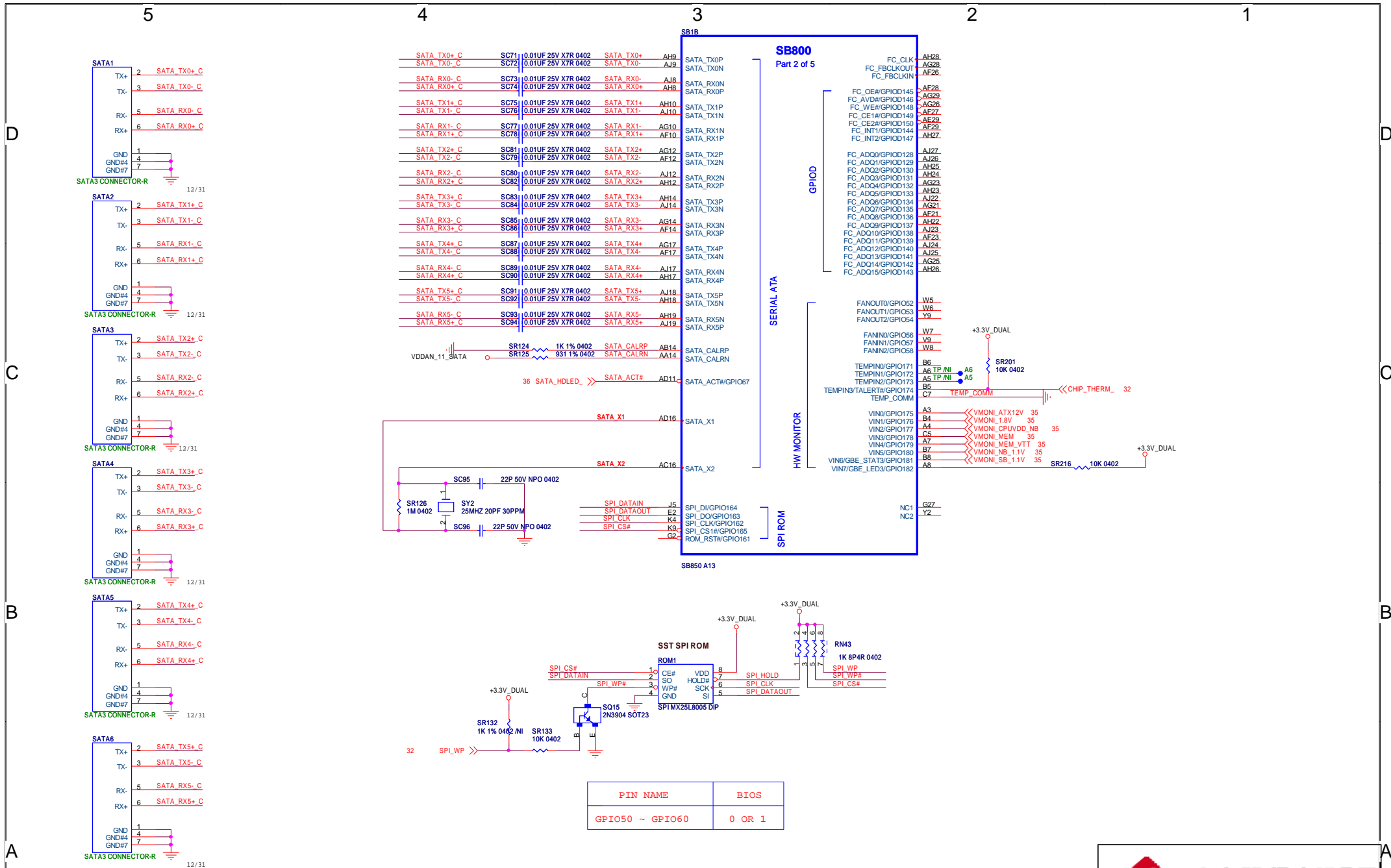


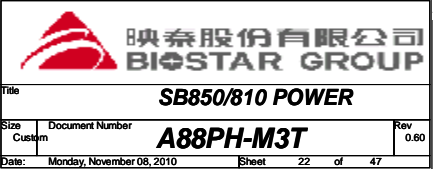
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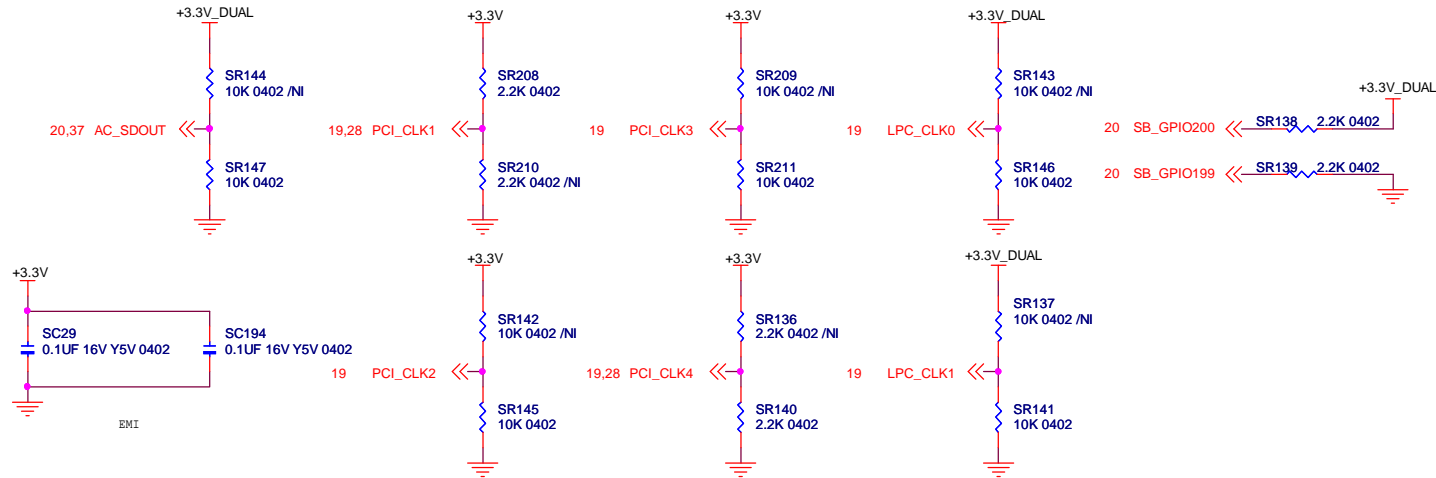




Title				SB850/810 ACPI/GPIO/USB/AUD			
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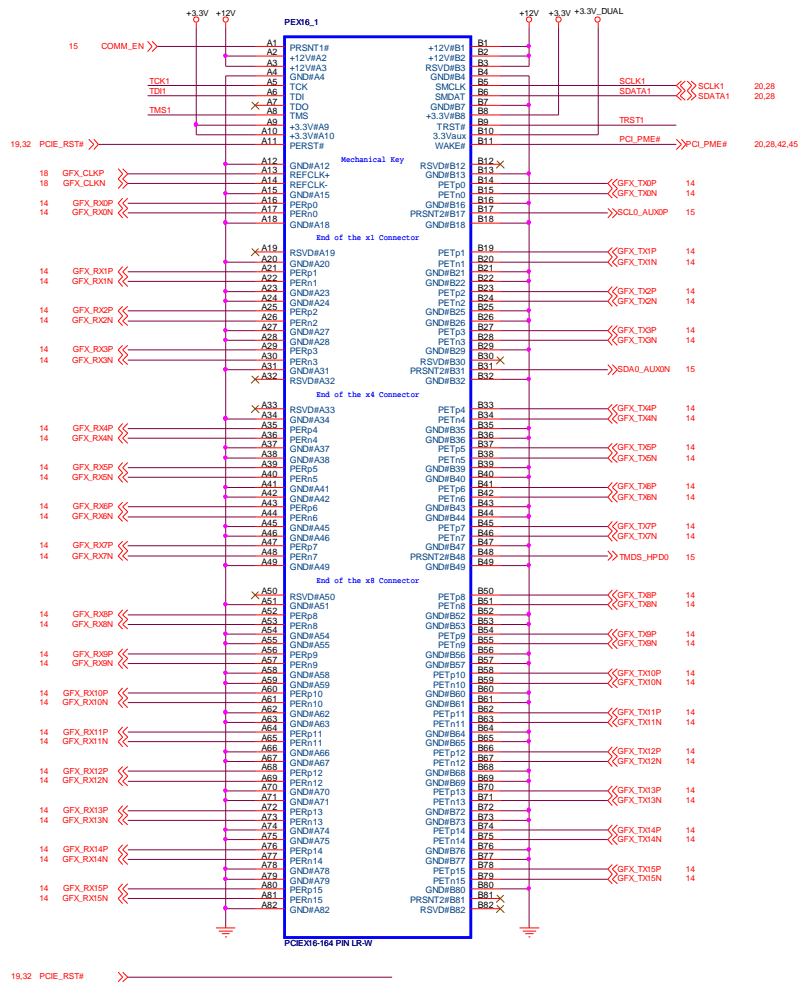
REQUIRED STRAPS

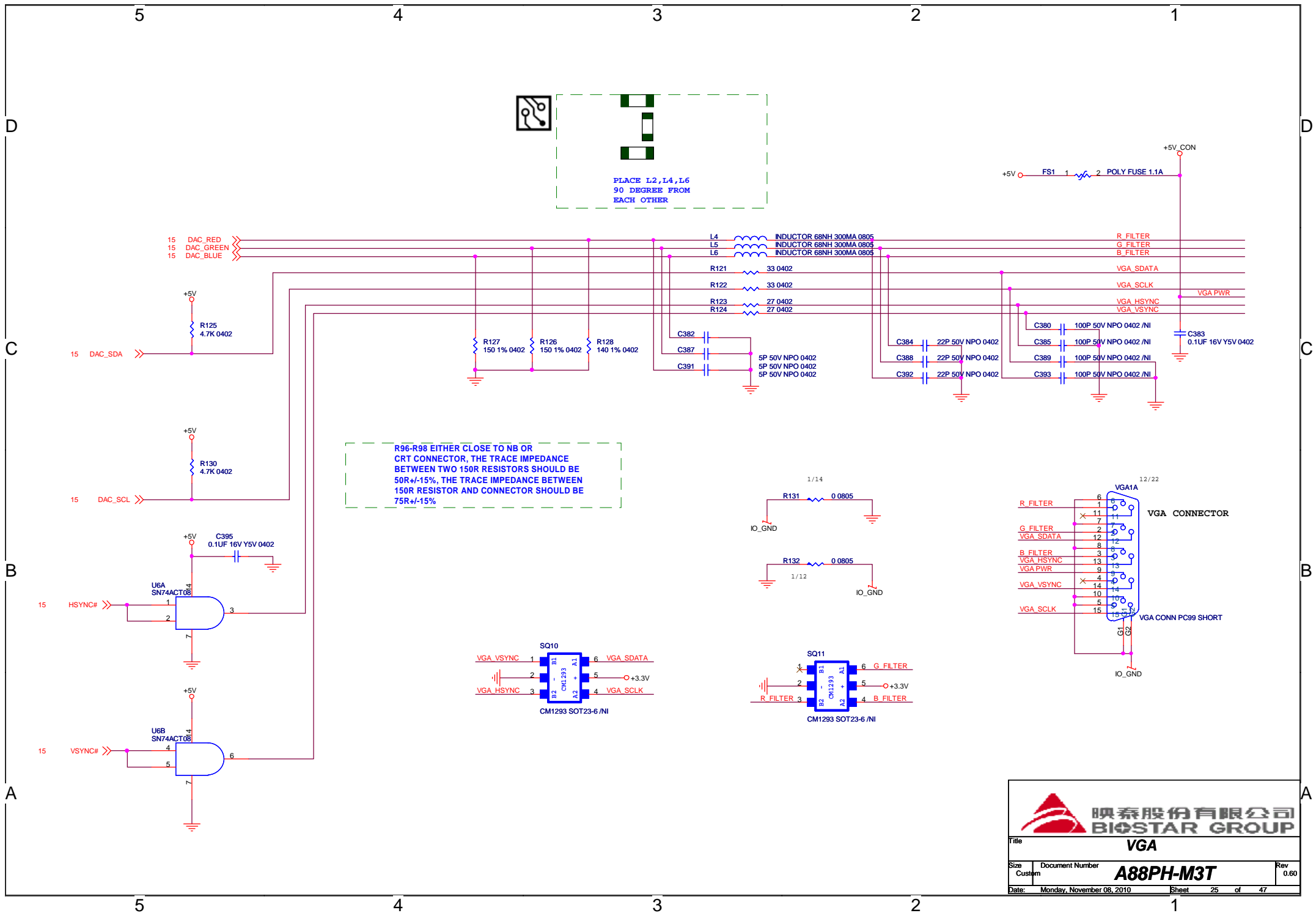
NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

	AZ_SDOUT	PCI_CLK1	PCI_CLK2	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	GPIO200	GPIO199
PULL HIGH	LOW POWER MODE	ALLOW PCIE GEN2 DEFAULT	WATCHDOG TIMER ON NB_PWRGD ENABLED	USE DEBUG STRAPS	NON-FUSION CPU CLOCK MODE	EC ENABLED	CLKGEN ENABLED	ROM TYPE: H, L = SPI ROM	DEFAULT
PULL LOW	PERFORMANCE MODE DEFAULT	FORCE PCIE GEN1	WATCHDOG TIMER ON NB_PWRGD DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT	FUSION CPU CLOCK MODE DEFAULT	EC DISABLED DEFAULT	CLKGEN DISABLED DEFAULT		



Title			SB850/810 STRAP		
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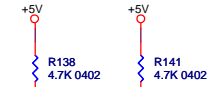
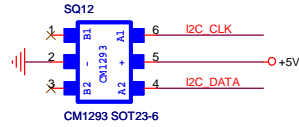
5

4

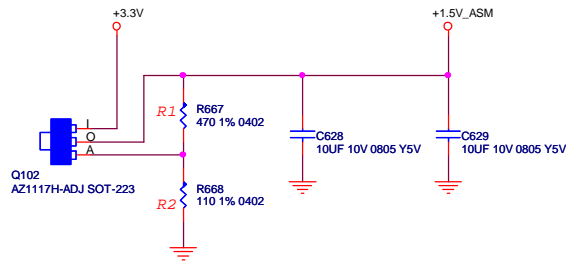
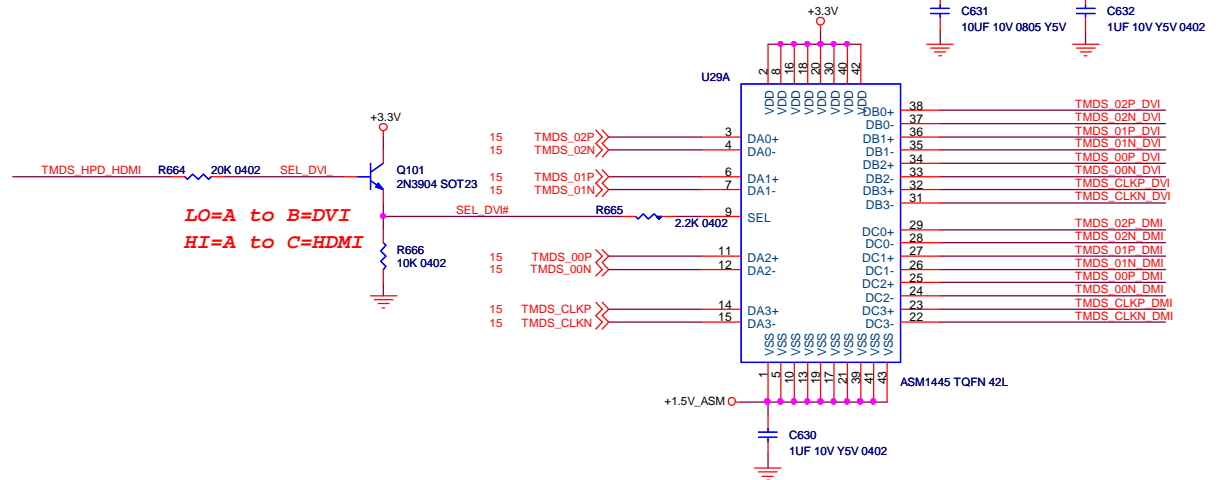
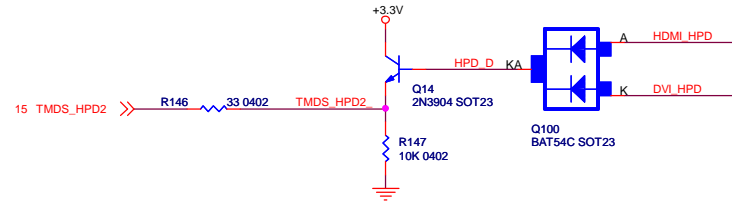
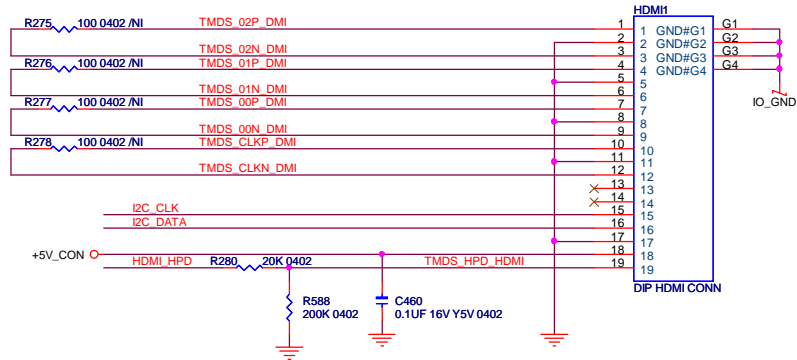
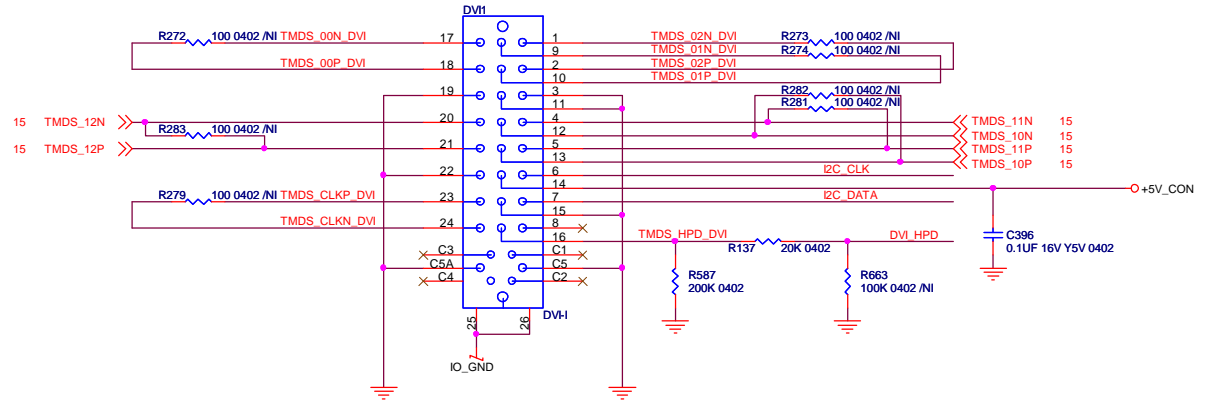
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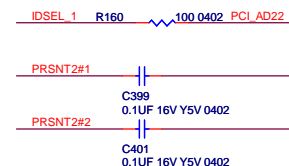
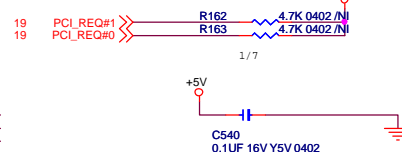
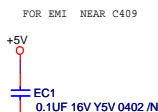
2

1



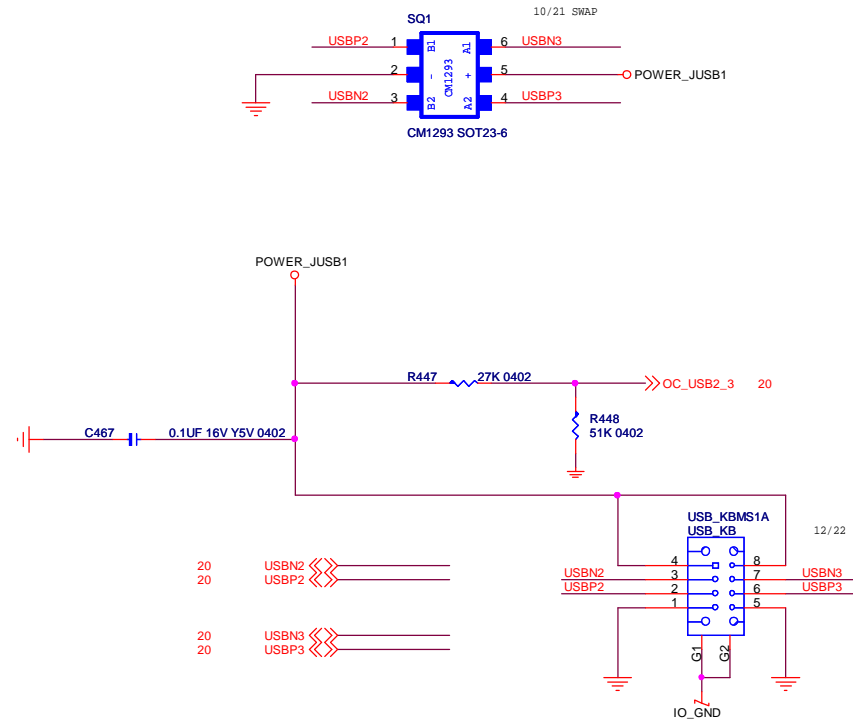
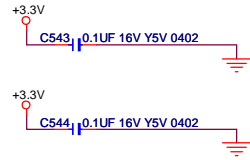
I2C_CLK <-> I2C_CLK
I2C_DATA <-> I2C_DATA



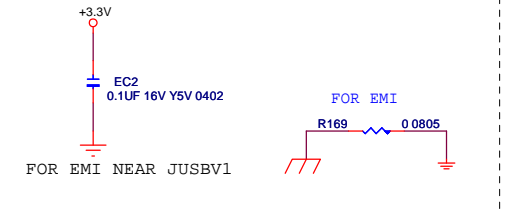
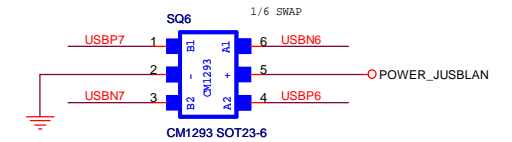
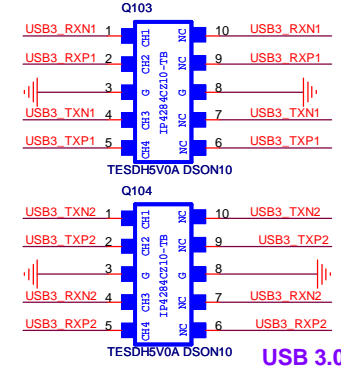
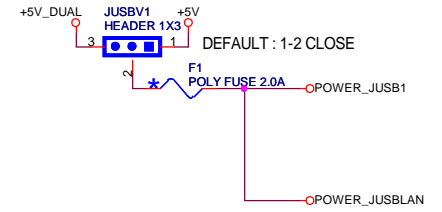
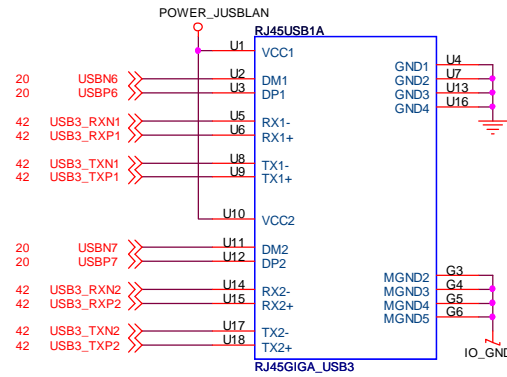
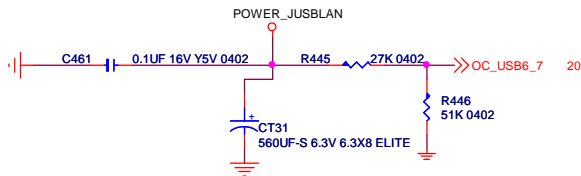


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BACK PANEL USB



BACK PANEL USB PLACE NEAR CONN

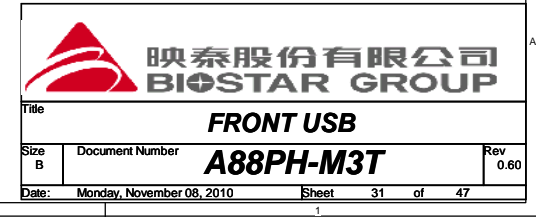
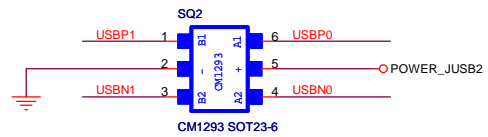
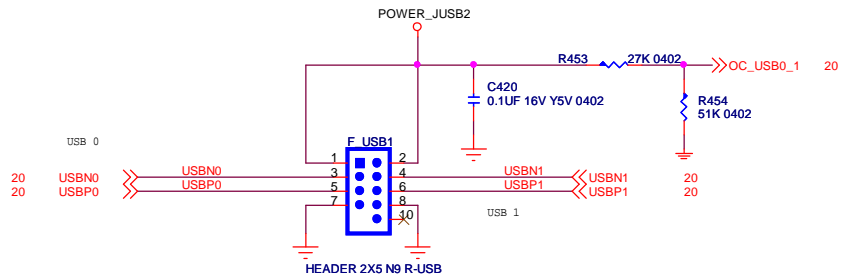
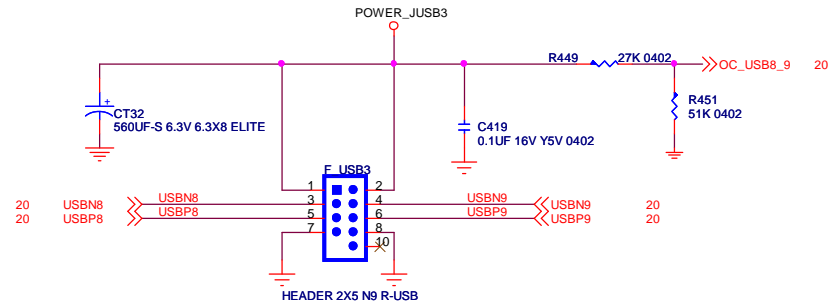
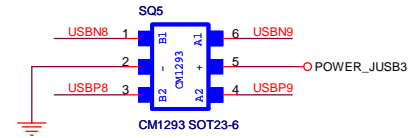
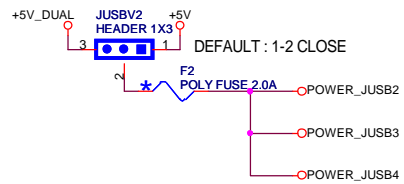
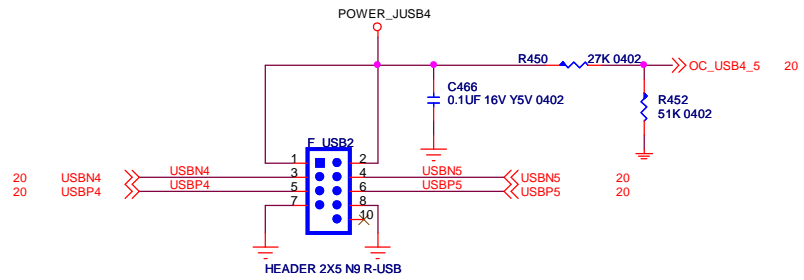
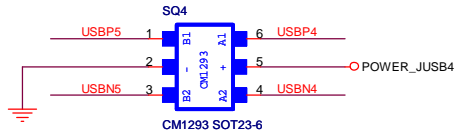


USB CONN

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FRONT PANEL USB



+3.3V

- SRN3 4.7K 8P4R 0402 10/22 SMDP
- RTS1# IO JP2
- SOUTA IO JP3
- DTR1# IO JP4
- SR55 1K 0402 PCIE_RST# RR
- SR8 1K 0402 PWRGD 30
- SR2 1K 0402 PWRGD 50
- SR4 1K 0402 PWRGD 150
- SR10 4.7K 0402 ATX_PG
- SR18 4.7K 0402 NI LPC_FRAME

+3V3_STBY

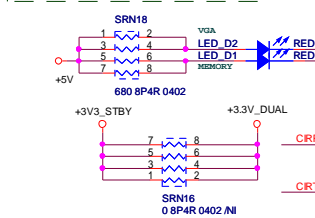
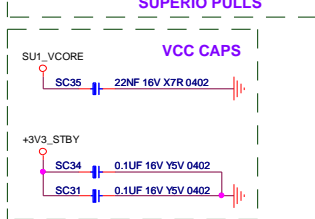
- SR272 4.7K 0402 NI PWRBTN
- SR9 1K 0402 5VSB_CTRL

+3.3V_DUAL

- SR50 4.7K 0402 R11# IO
- SR11 10K 0402 RSMRST#
- SR54 10K 0402 PWRON# JP8

+5V_STBY

- SR274 10K 0402 PS_ON



SO19
AZ1117H-ADJ SOT-223
SC12
100UF-5 16V 6.3X8 ELITE

+5V_STBY

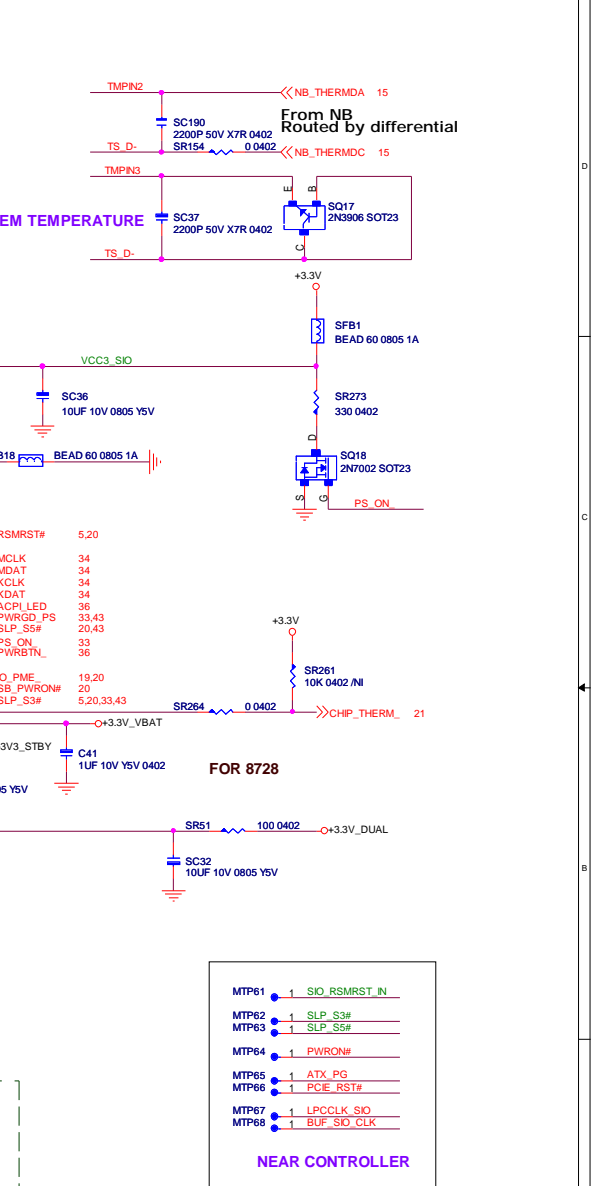
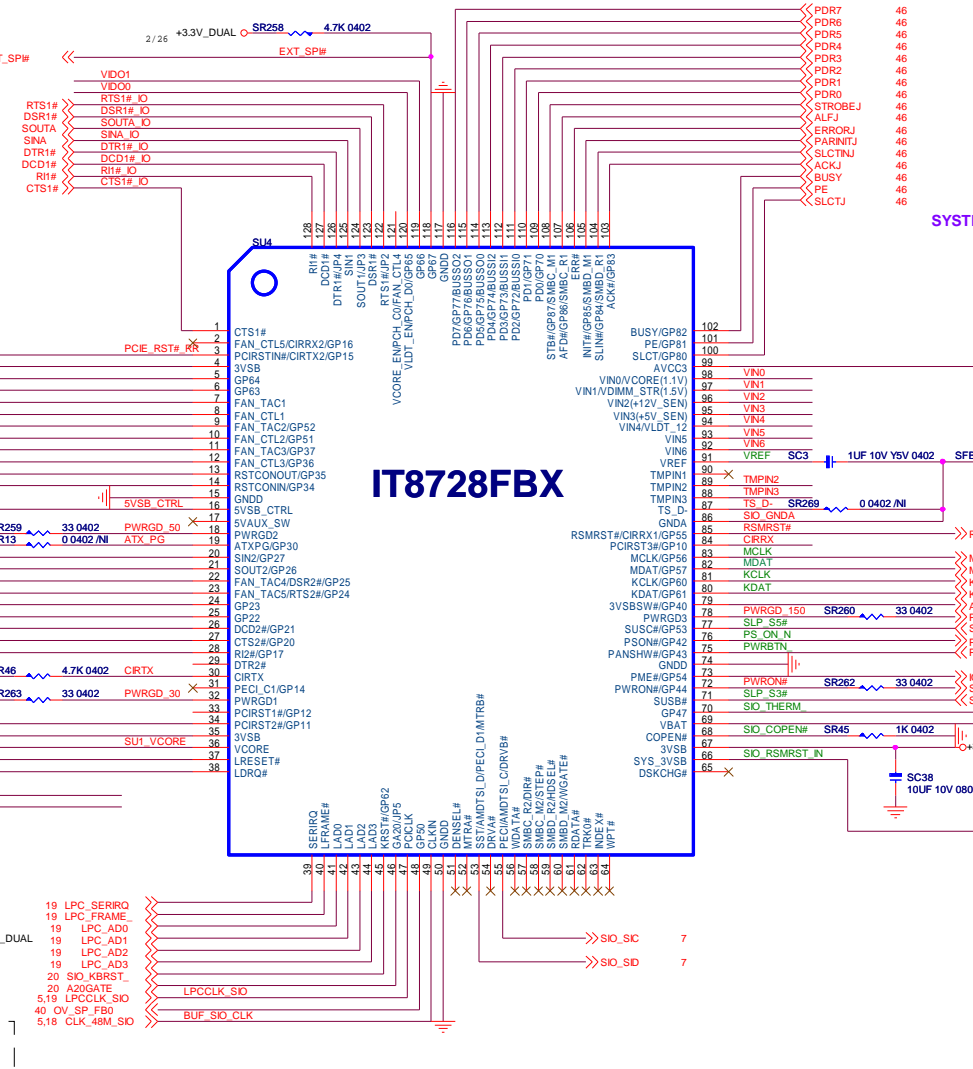
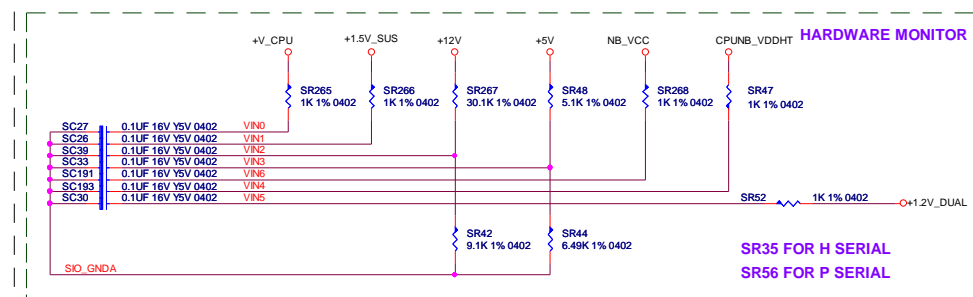
+3V3_STBY

SR58
200 1% 0402

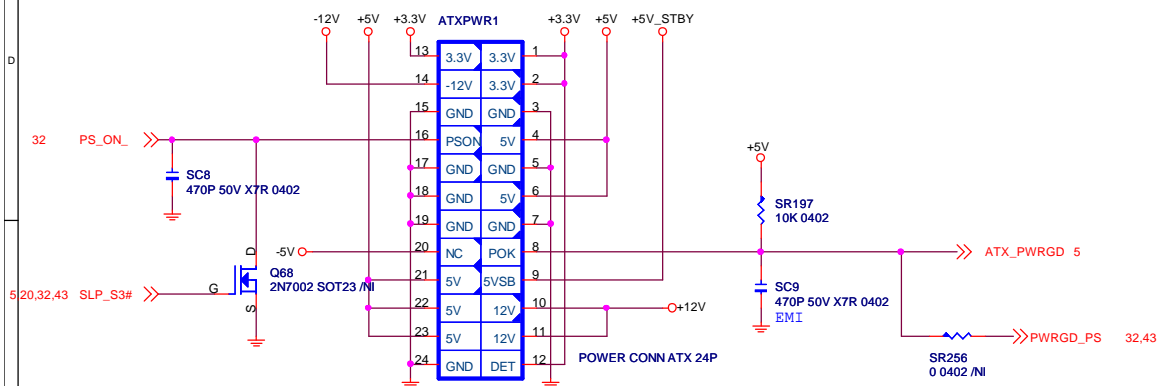
SR59
330 1% 0402

SC40
10UF 10V 0805 Y5V

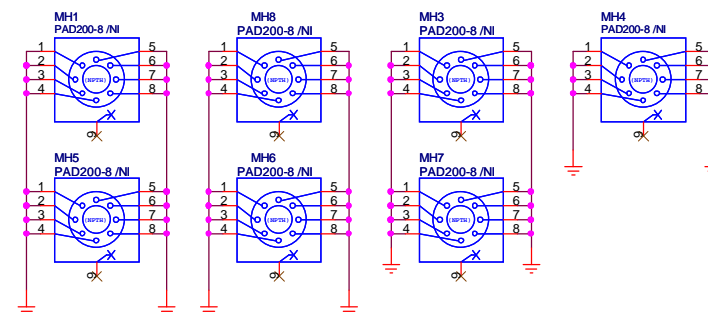
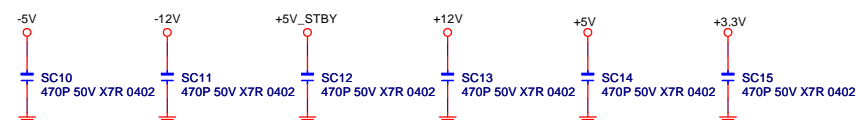
$$V_{out} = V_{ref} (1.25V) \times (1 + R2/R1)$$

$$= 3.39V$$


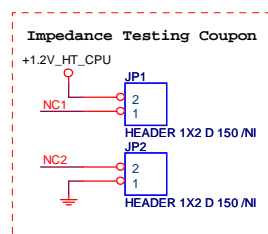
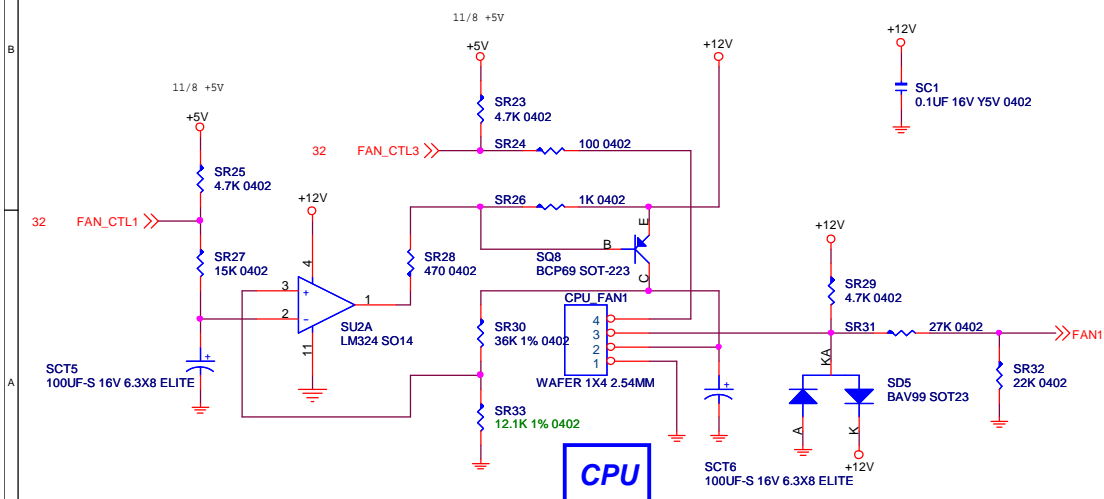
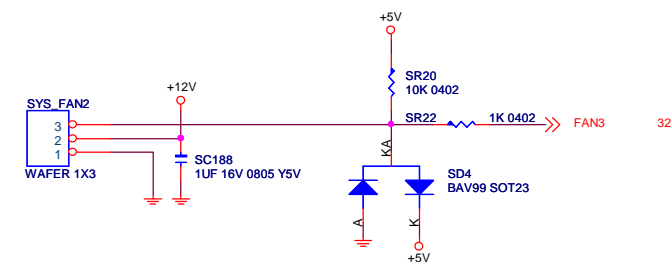
POWER CONNECTOR



EMI POWER CONN DECOUPLING

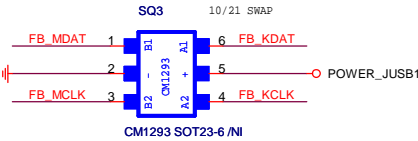
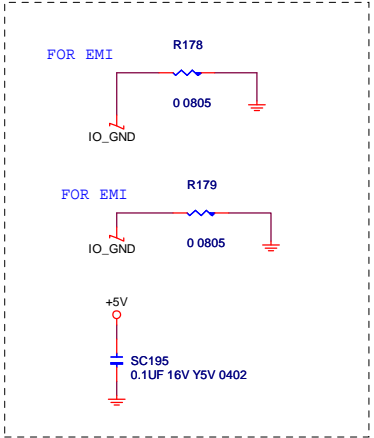
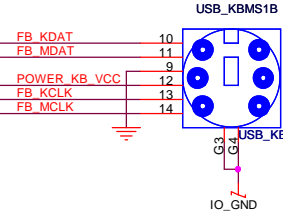
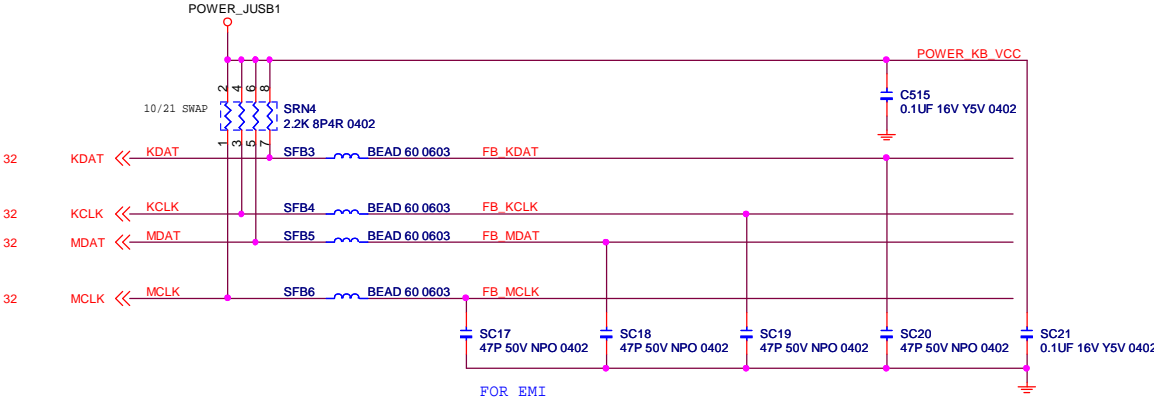



SYSTEM



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FOR KEYBOARD WITH MOUSE CONN.





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BIOSAR GROUP

Title

PS2 CONN

Size B

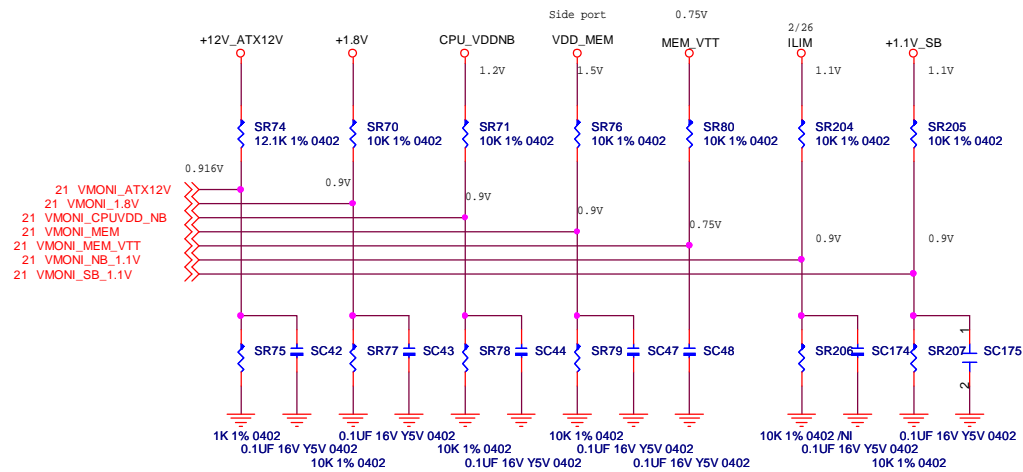
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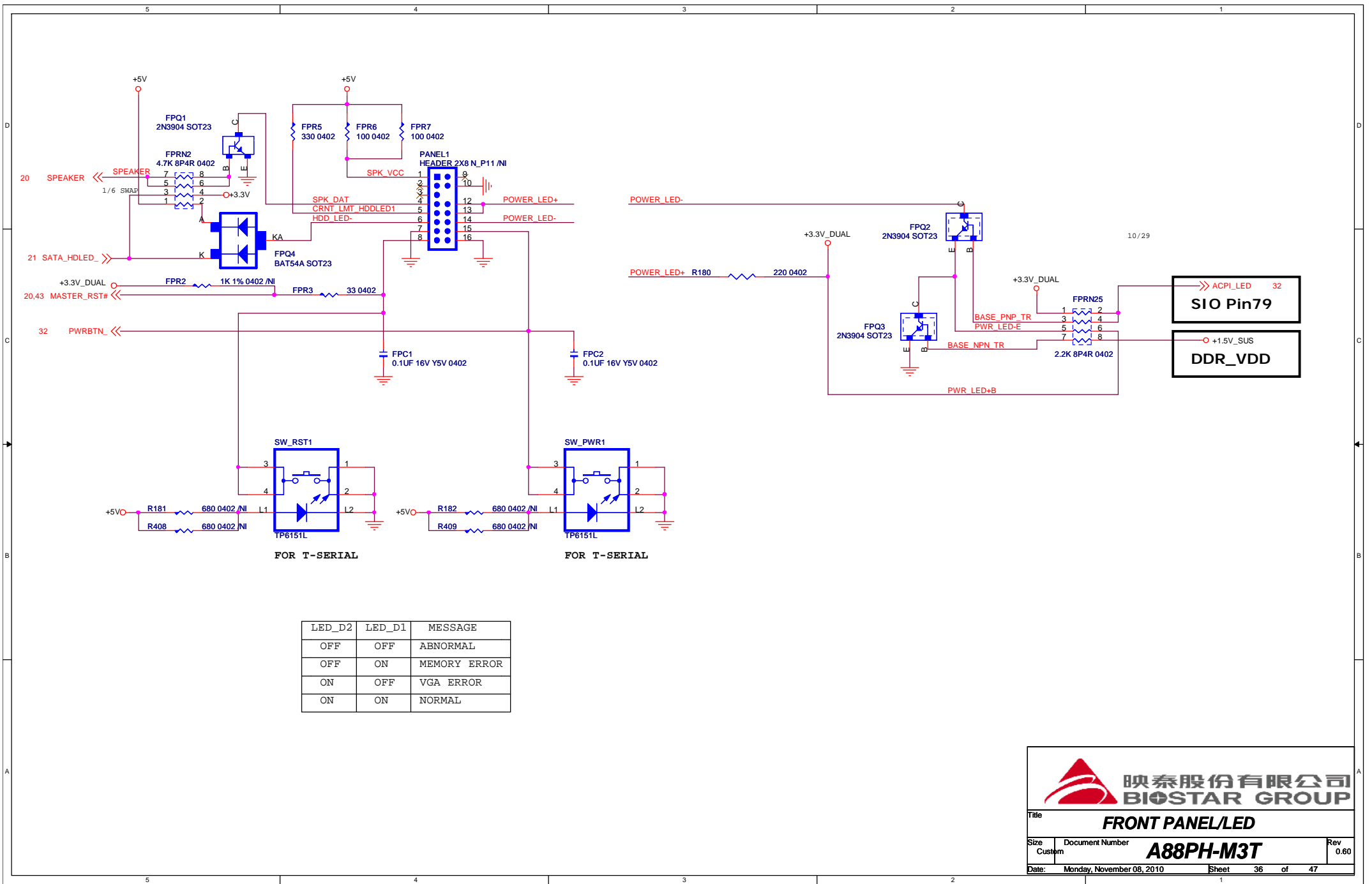
Rev 0.60

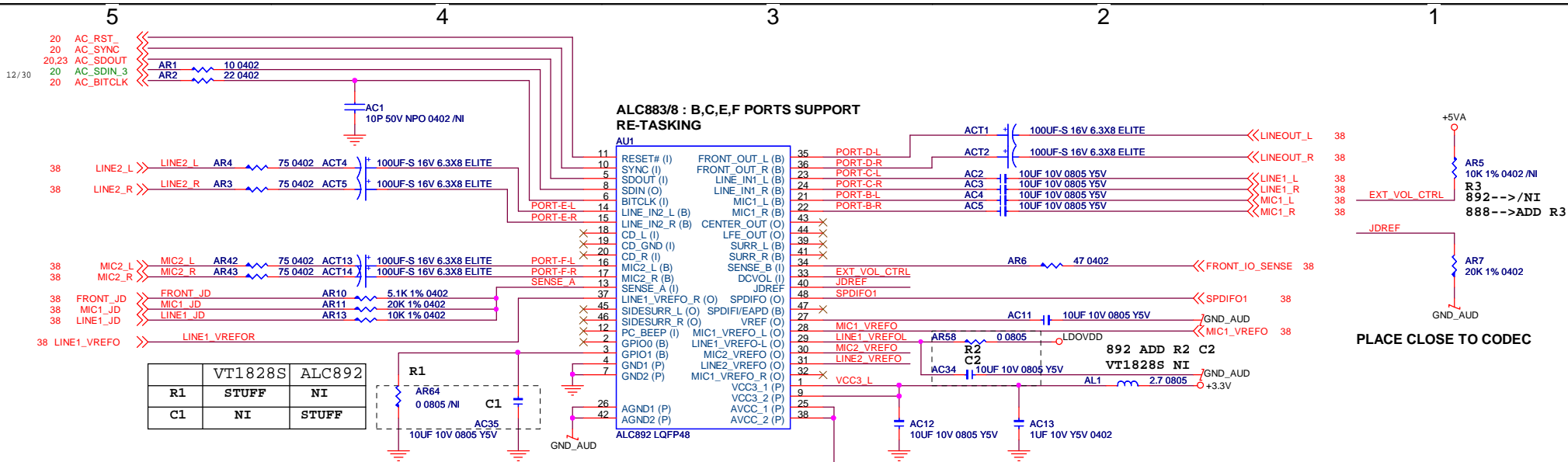
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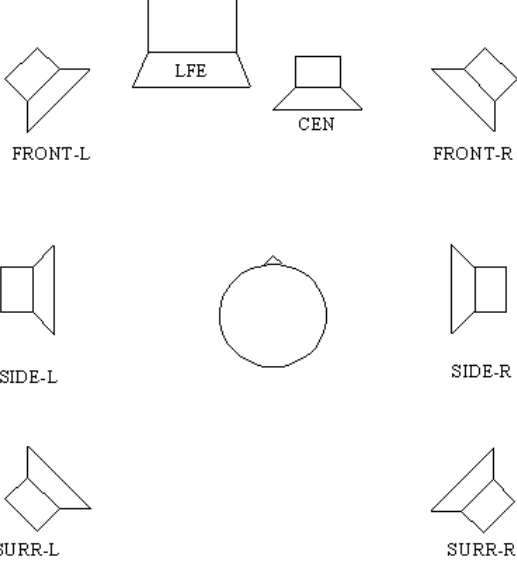


		
Title: H/W MONITOR		
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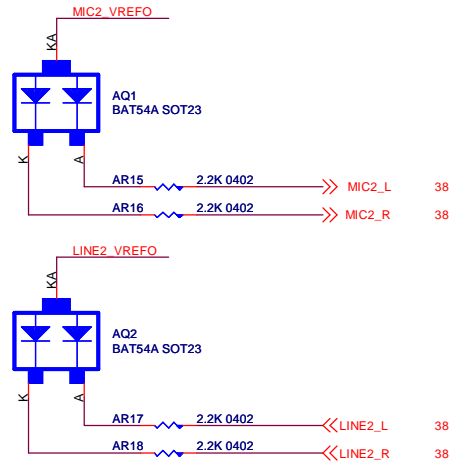
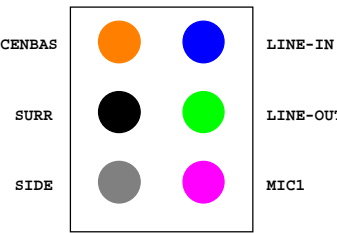
7.1 Speaker Configuration



Configuration

	PORT-A	PORT-B	PORT-C	PORT-D	PORT-E	PORT-F	PORT-G	PORT-H
Function	SURR	MIC1	LINE1	LINEOUT	LINE2	MIC2	CEN/LFE	SIDE
Location	Rear	Rear	Rear	Rear	Front	Front	Rear	Rear

	VT1828S	ALC892
PIN3	GPIO1	REGREF
PIN4	DVSS	DMIC_DATA/GPIO
PIN29	VREFOUT-C	LDO-IN



Title: **EUP POWER**

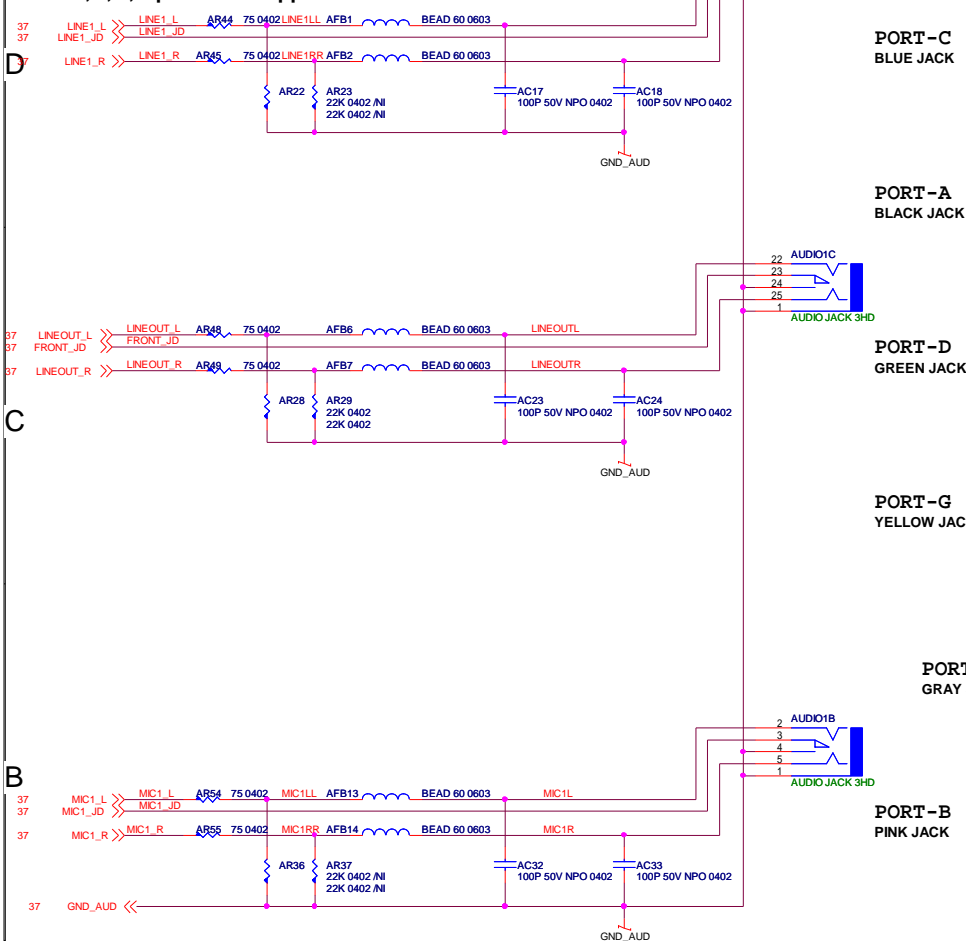
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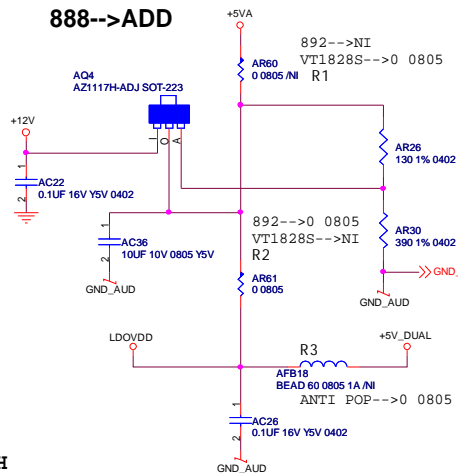
Rear Panel Onboard Analog I/O P07 : 3-Port & 6-Port Co-lay

ALC883/888:B,C,E,F ports support I/P & O/P function.

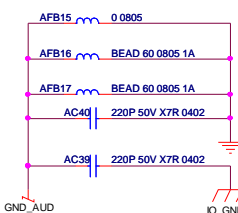
But A,D,G,H ports not support MIC function.



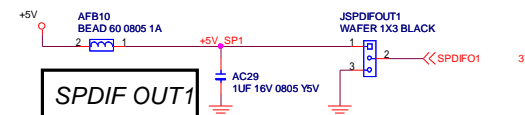
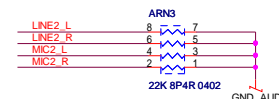
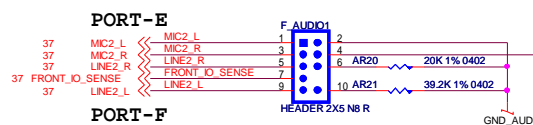
AUDIO ANALOG POWER 892-->/NI 888-->ADD



FOR EMI

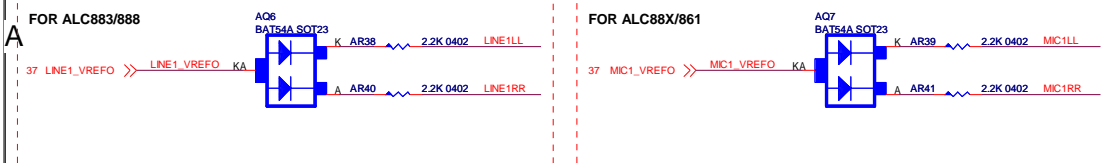


PORT-E PORT-F



V0.51 Add EMI component CM1293 SOT23-6

	ALC883/8	ALC861VD
HDA Interface	Yes	Yes
DAC	5 DAC 7.1Ch + 2 Ch	4 DAC 7.1Ch
DAC SNR	95dB	90dB
GPIO for Vref	None	None
SPDIF-I	Yes	No
SPDIF-O	Yes	Yes
Re-Tasking	4-PORTS	Port D,E,F



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Title: EUP POWER

Size: Custom

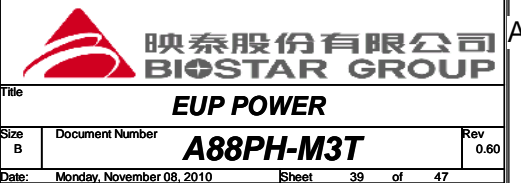
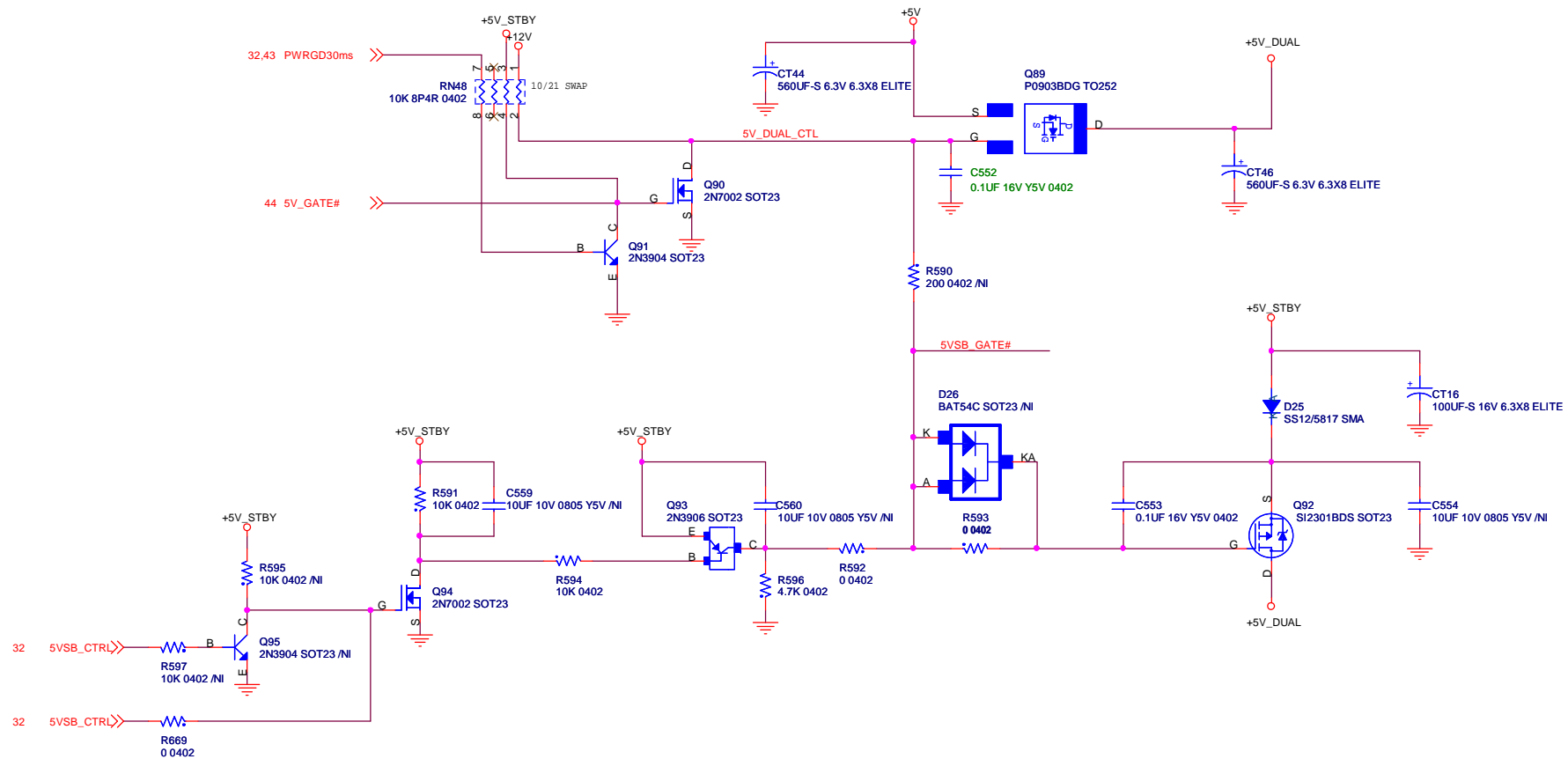
Document Number: A88PH-M3T

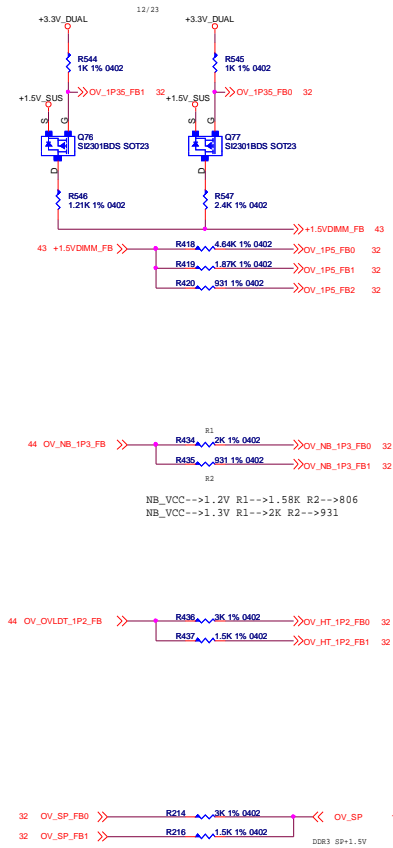
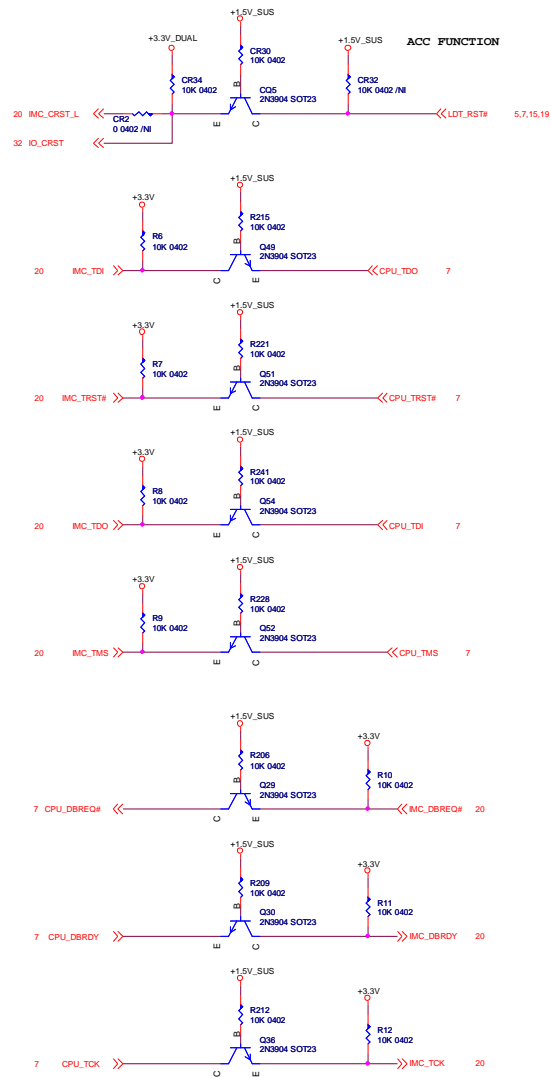
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Energy-Using Product(EUP)





+1.5VDIMM_FB	VDIMM0	VDIMM1	VDIMM2
Default 1.509V	1	1	1
1.547V	0	1	1
1.605V	1	0	1
1.644V	0	0	1
1.703V	1	1	0
1.742V	0	1	0
1.799V	1	0	0
1.838V	0	0	0

+1.5VDIMM_FB	OV_1P35_FB1	OV_1P35_FB0
1.353V	0	0
1.397V	0	1
1.447V	1	0
Default 1.509V	1	1

	OV_NB_1P1_FB0	OV_NB_1P1_FB1
+1.2V / +1.296V	1	1
+1.250V / +1.346V	0	1
+1.299V / +1.403V	1	0
+1.349V / +1.452V	0	0

	OV_SB_1P1_FB0	OV_SB_1P1_FB1
+1.204V	1	1
+1.246V	0	1
+1.284V	1	0
+1.324V	0	0

SIDEPORT	GP27	GP26
Default 1.51V	1	1
1.55V	0	1
1.6V	1	0
1.65V	0	0

6717X : Remove R4
 K8_VID0 >> PR221 0.0402/N OS_CORE PR222 120K 0402/N PR223 68K 0402/N
 R4
 6717A OLD Short to GND:NO Use SVI Mode
 Pull UP to 3V3:Negative Analog OS
 Pull Down to GND:Positive Analog OS
 6717X Short to GND:Analog OS Disable
 Pull UP to 3V3:Negative Analog OS
 Pull Down to GND:Positive Analog OS

6717X : Remove R5
 K8_VID1 >> PR225 0.0402/N STRAP PR226 10K 0402/N PR227 0.0402/N
 R5
 6717A OLD Short to GND:SVI Mode Trigger
 6717X

STRAP VOLTAGE	I2C Bus	NB LL	PSK
0.0<STRAP<0.4	EN	EN	EN
0.4<STRAP<1.2V	DIS	EN	EN
1.2<STRAP<2.0V	DIS	DIS	EN
2.0<STRAP<3.0V	DIS	EN	DIS
3.0<STRAP<3.3V	DIS	DIS	DIS

R6
 7 CORE_TYPE >> PR228 0.0402/N
 7 K8_VID1 >>
 6717A OLD Short to GND:SVI Mode Trigger
 6717X : Remove R6

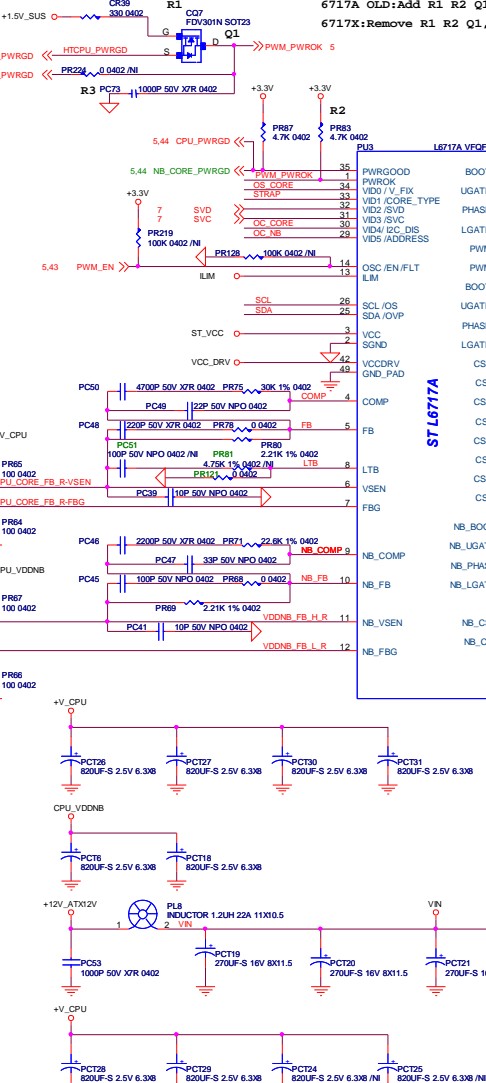
7 CPU_CORE_FB >> PR106 0.0402
 7 CPU_CORE_FB >> PR77 0.0402
 7 CPU_CORE_FB >> PR106 0.0402
 7 CPU_CORE_FB >> PR77 0.0402
 7 VDDNB_FB_H >> PR115 0.0402
 7 VDDNB_FB_L >> PR116 0.0402

6717A NEW : Remove R7
 K8_VID4 >> PR229 0.0402/N OC_CORE PR230 25K 0402/N PR231 10K 1% 0402/N
 R7
 6717A OLD Short to GND:I2C Enable
 Pull UP to 3V3:I2C Disable
 6717X Program the OC TDC for CORE
 Pull Down to GND and Filter with MLCC

6717X : Remove R8
 K8_VID5 >> PR232 0.0402/N OC_NB PR233 10K 0402/N PR234 10K 1% 0402/N
 R8
 6717A OLD Short to GND:I2C ADDRESS=0
 Pull UP to 3V3:I2C Disable=1
 6717X Program the OC TDC for NB
 Pull Down to GND and Filter with MLCC

ILM
 6717A OLD Short to GND:I2C Enable
 Pull UP to 3V3:I2C Disable
 6717X Program the OC TDC for CORE
 Pull Down to GND and Filter with MLCC

11,12,18,20 SCLK >> PR236 0.0402 SCL PR237 68K 0402/N
 11,12,18,20 SDATA >> PR238 0.0402 SDA PR239 25K 0402/N PR240 0.0402/N
 6717A OLD I2C Enable:Pull UP to 3V3
 I2C Disable:Short to GND:Analog OS Disable
 I2C Disable:Pull UP to 3V3:Negative Analog OS
 I2C Disable:Pull Down to GND:Positive Analog OS
 6717X I2C Enable:Pull UP to 3V3
 Not Used:Pull UP to 3V3



ST L6717A

6717A OLD: Add R1 R2 Q1, Remove R3
 6717X: Remove R1 R2 Q1, Add R3

6717A NEW: Remove R7
 6717X: Remove R8

6717A OLD: Short to GND: I2C Enable
 Pull UP to 3V3: I2C Disable
 6717X: Program the OC TDC for CORE
 Pull Down to GND and Filter with MLCC

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 Pull UP to 3V3: I2C Disable=1
 6717X: Program the OC TDC for NB
 Pull Down to GND and Filter with MLCC

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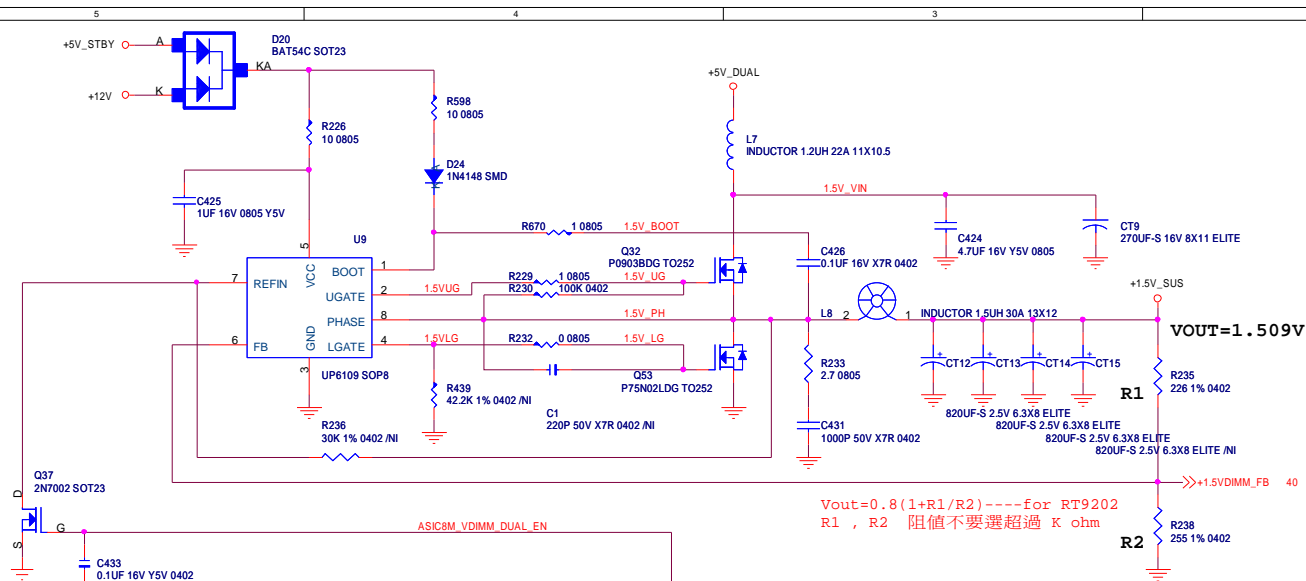
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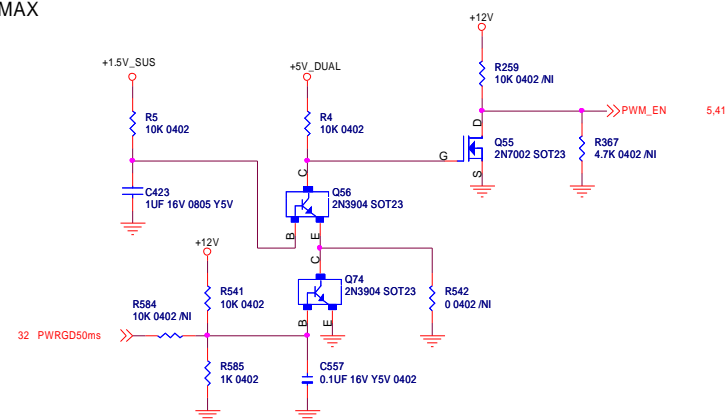
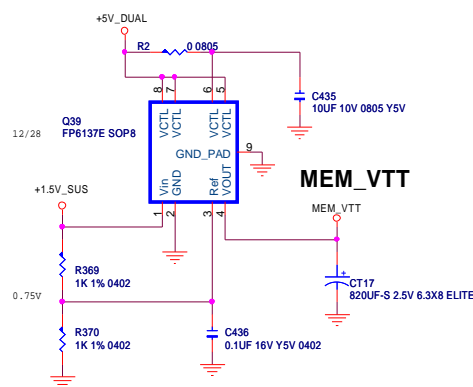
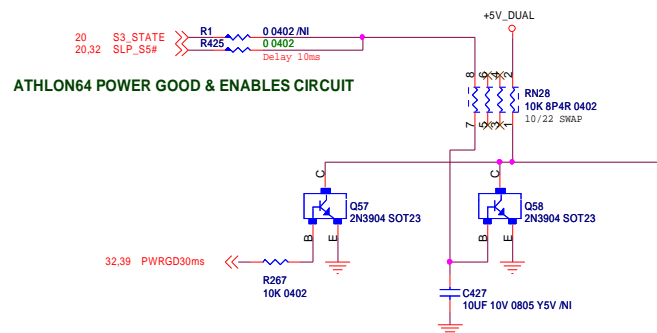
INDUCTOR 1UH 30A = DCR 1.4m
 INDUCTOR 1.5UH 30A 13X12 = DCR 1.85m
 820UF-S 2.5V 6.3X8 = ESR 7m OUTPUT Cap. X 7 pcs
 270UF-S 16V 8X11.5 = ESR 11m INPUT Cap. X 4 pcs

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+1.5V @ 20A AMPS MAX

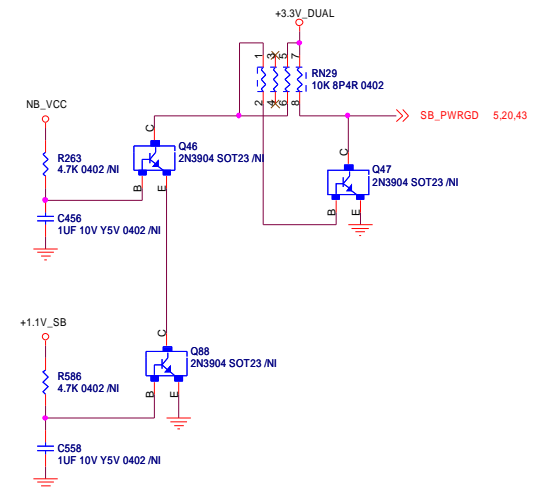
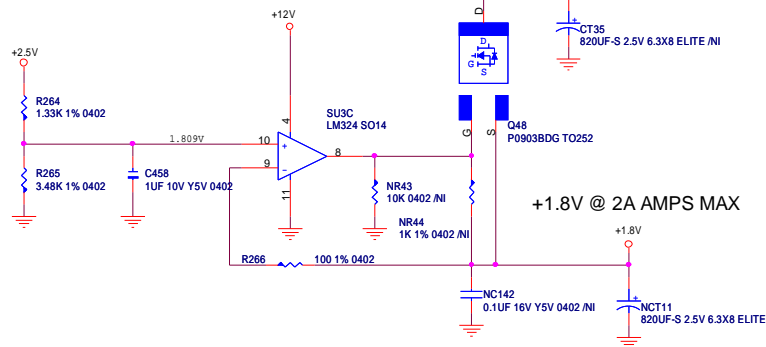
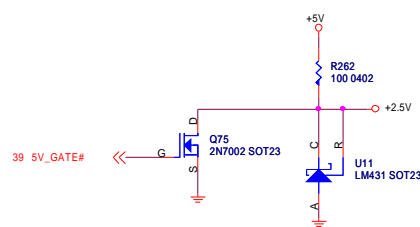
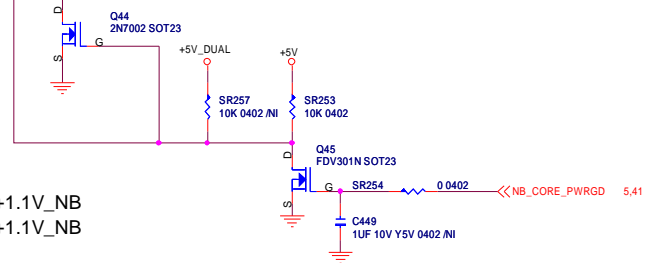
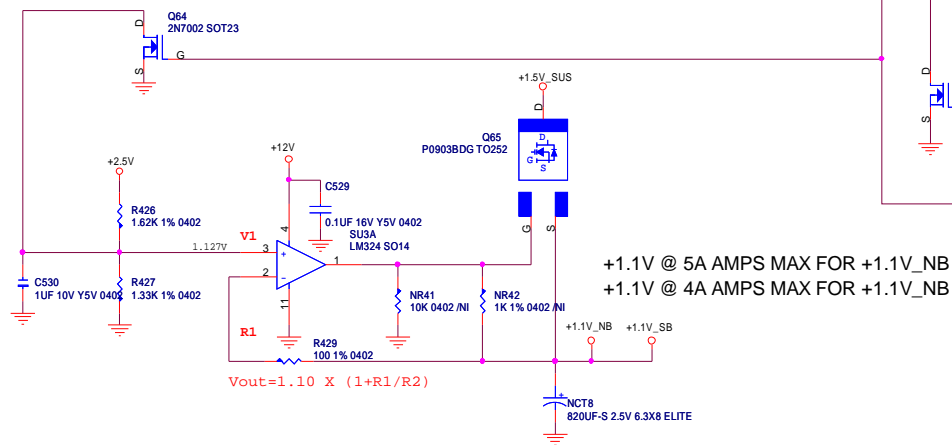
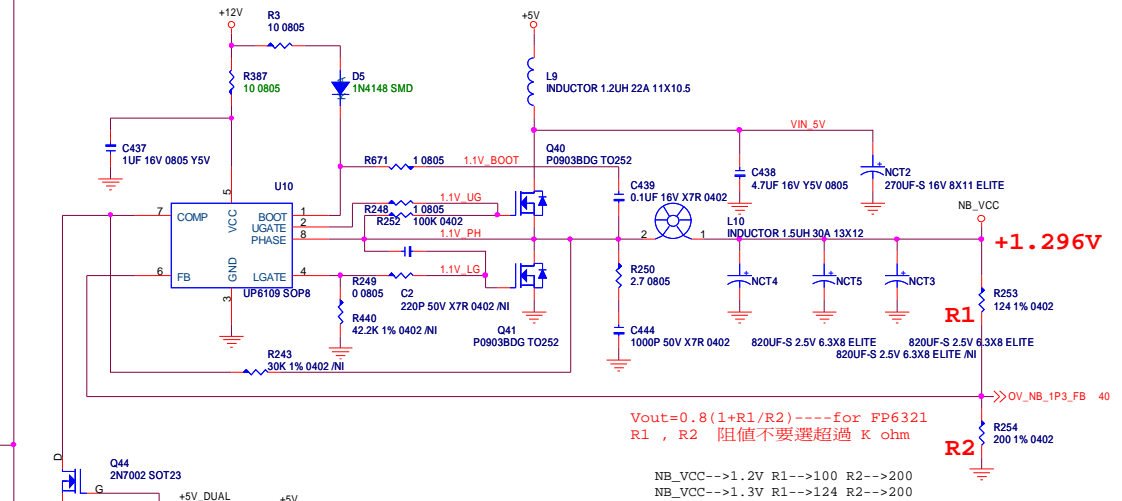
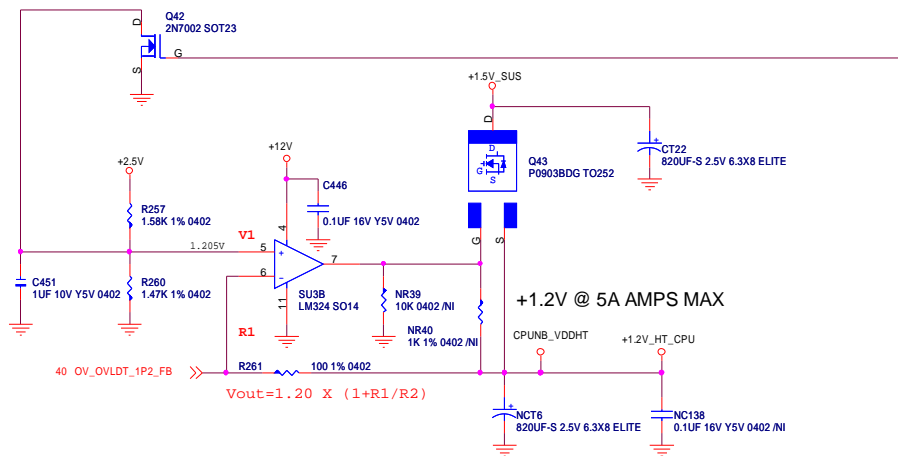


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4

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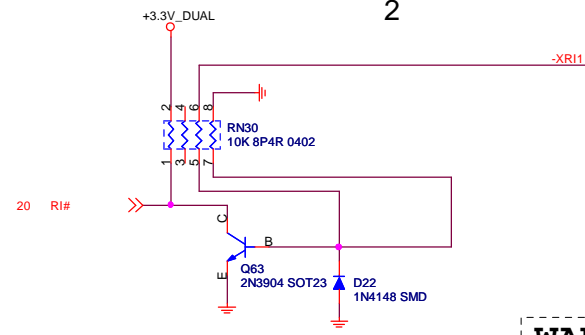
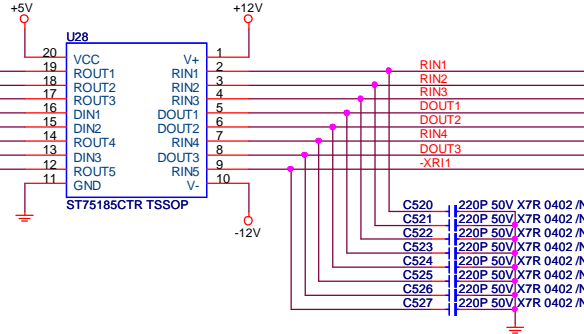
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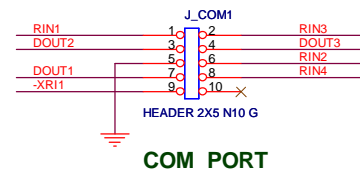
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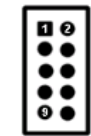
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WAKE ON LAN

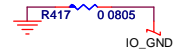
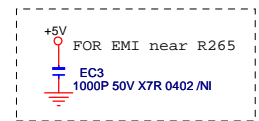
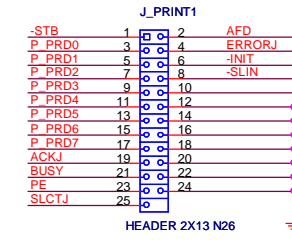
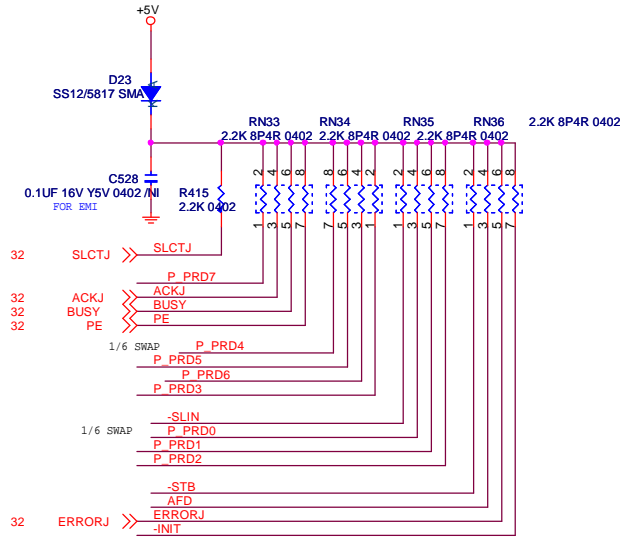
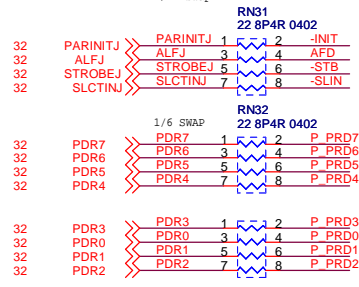



COM PORT



COM PORT PIN
ASSIGNMENT

1/12 swap





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Title
1394

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